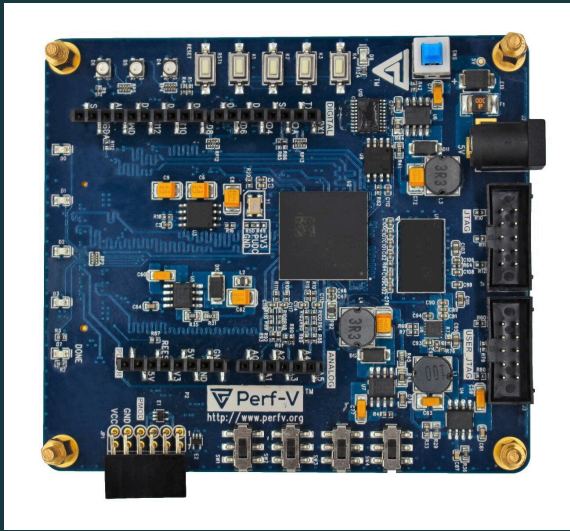
seeed studio

INDUSTRIAL PRODUCT DATASHEET

Perf-V Based on Xilinx Artix-7 FPGA RISC-V opensource

SKU	UPDATED	PRICE
102110277	2026-11-11 08:20:38	$79.00(Excl. VAT)



Key Features

01 **It uses Xilinx Artix-7 FPGA, Vivado software development,and is designed for the RISC-V open source community and FPGA learning enthusiasts design development board.**

02 **It**

Integrates a variety of peripheral chips to provide a rich set of peripheral interfaces, including PMOD, Arduino, JTAG, UART interfaces, and high-speed interfaces for expansion of HDMI, VGA, USB2.0/3.0, camera, Bluetooth, expansion boards, etc. Strong flexibility.

03 **Based on Perf-V's self-developed smart car, it can use mobile phone Bluetooth to control the movement of the car, and can realize automatic tracing and obstacle avoidance functions.**

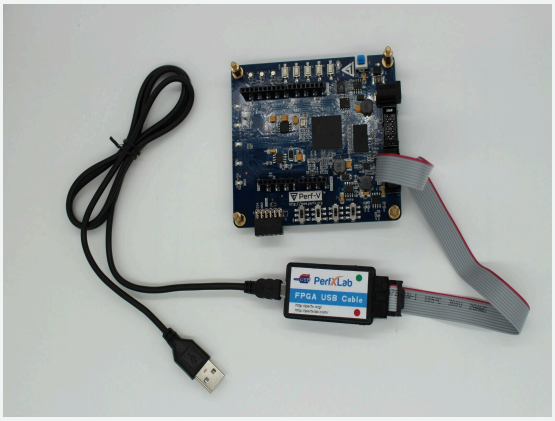
04 **It successfully ports a variety of RISC-V architectures, providing a good experimental platform for RISC-V processor design and FPGA product development, and is the preferred hardware for learning, scientific research, project development, and DEMO solutions.**

05 **There is a variety of modules available for selection, powerful, rich learning materials, complete experimental routines, cost-effective;**

06 **It is for electronic design engineers, hardware DIY enthusiasts, all kinds of enthusiasts, college teachers and students and other groups.**

Product Gallery





Resource Links

- https://www.seeedstudio.com/Perf-V-Based-on-Xilinx-Artix-7-FPGA-RISC-V-opensource-p-4058.html
- Documentation & Download

Seeed Technology Co.,Ltd.
9F, Building G3, International E City,
Zhongshanyuan Road, Nanshan, Shenzhen, China

Tel: +86 0755-80695676
Web: www.seeed.cc
Shop: www.seeedstudio.com