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INDUSTRIAL PRODUCT DATASHEET

Sipeed Tang Nano FPGA Board Powered by GW1N-1 FPGA

Sipeed Tang Nano FPGA board powered by GW1N-1 FPGA board carries a low power core chip GW1N-1-LV, this chip has 1152 LUT4 RAM, 864 FF(Flip-Flop), 72K Block SRAM.

SKU	UPDATED	PRICE
102991314	2026-11-11 08:22:56	$5.90(Excl. VAT)



Overview



Sipeed Tang Nano FPGA board powered by GW1N-1 FPGA board carries a low power core chip GW1N-1-LV, this chip has 1152 LUT4 RAM, 864 FF(Flip-Flop), 72K Block SRAM. This board is compatible with a 5 inches 800*480 screen for the FPGA board.

Key Features

01 **GW1N-1-LV**
1152 LUT4, 864 FF(Flip-Flop)

02 **72K B-SRAM(bits); 96K user flash(bits); 1 PLL**

03 **Support a 5 inches 800*480 screen**

04 **Onboard JTAG Downloader**

05 **Not support for DSP**

06 **Small size, low cost**

Resource Links

- https://www.seeedstudio.com/Sipeed-Tang-Nano-FPGA-board-powered-by-GW1N-1-FPGA-p-4304.html
- Tang nano datasheet
- IDE
- License
- Github
- Wiki
- SDK Information
- HDK Information

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