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INDUSTRIAL PRODUCT DATASHEET

DSLogic Pro

DSLogic Pro is an open-source, FPGA-based logic analyzer offering up to 400MHz sampling rate, 16 channels, and expandable functionality including oscilloscope and protocol analysis.

SKU	UPDATED	PRICE
109990074	2024-08-09 01:56:53	$99.00(Excl. VAT)



Overview



DSLogic Pro is an upgrade from the DSLogic project, featuring an open-source design, unibody aluminum shielded case, and an FPGA-based core with up to 400MHz sampling rate and 256Mbit on-board memory. It functions as a 16-channel logic analyzer with continuous adjustable threshold, and can be extended via modules to serve as an oscilloscope, data acquisition system, protocol analyzer, and more.

Key Features

DSLogic Pro is an upgrade product from DSLogic project.

DSLogic is an Open-Source, Low-cost & Extensible electronic instrument for makers and electronic enthusiasts, which consists of DSLogic-Core and extension modules. DSLogic-Core is a re-configurable circuit board based on FPGA technology with maximum 400MHz sampling rate and 256Mbit on-board memory. DSLogic-Core can work alone as a fully functional logic analyzer, along with various extension modules. DSLogic can also work as oscilloscope, data acquisition system, protocol analyzer and even RF analyzer.

DSLogic was successfully funded on KICKSTARTER.

Features

- Open source
- Unibody aluminum shielded case
- Upgradable hardware
- Continuous Adjustable threshold
- Up to 1G samples at stream mode
- Support more than 40 protocol analyzers
- Advanced triggering option support
- External trigger support
- State sample support
- USB interface, USB powered
- Extensible & Reconfigurable
- Shielded wire bundle included

Specifications

- Input channels number: 16
- Max Sampling rate: 400M @ 4 channels / 200M @ 8 channels / 100Mbps @ 16 channels
- Max Sampling depth: 1G/channel @ stream mode / 16M/channel @ buffer mode
- Hardware memory size: 256Mbits
- Threshold voltage: 0 ~ 5V (continuous adjustable, 0.1v step)
- Min pulse width can be captured: 5ns
- Input bandwidth: 50MHz
- Max state clock input: 50MHz
- Trigger position support
- Advanced trigger stages: 16 stages

- Advanced trigger count: 16 bits
- Advanced trigger set: 2 sets per stage
- Input voltage range: -30 ~ + 30V
- Input impedance: 250Kohm // ~7pF
- State sample support
- External trigger input support
- External trigger output support
- Support OS: windows, Linux, OS X
- Dimensions: 79 x 74 x 9mm

Part List

- DSLogic Pro: x 1
- Shielded wire bundles: x 1
- Clips for logic analyzer: x 16
- USB cable: x 1

Support

- **website**
- **wiki**
- **forum**

For technical support and suggestions, please visit our **forum**.

Product Gallery





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Compliance & Logistics

HSCODE	9030900090

Resource Links

- https://www.seeedstudio.com/DSLogic-Pro-p-2423.html

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