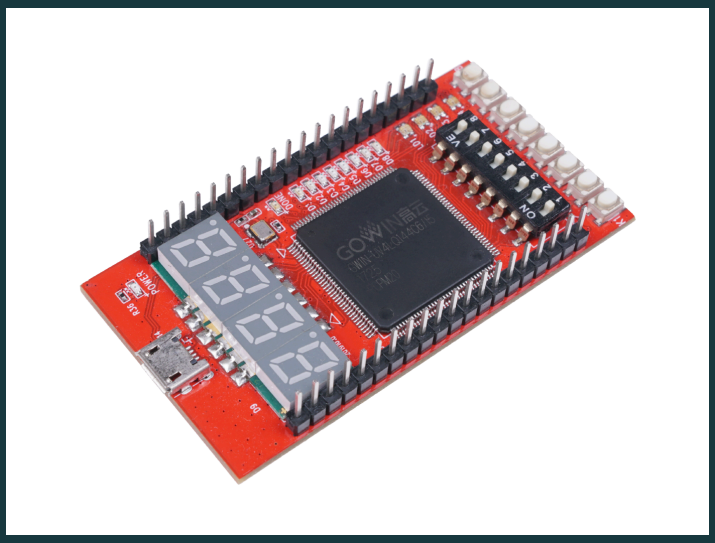
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INDUSTRIAL PRODUCT DATASHEET

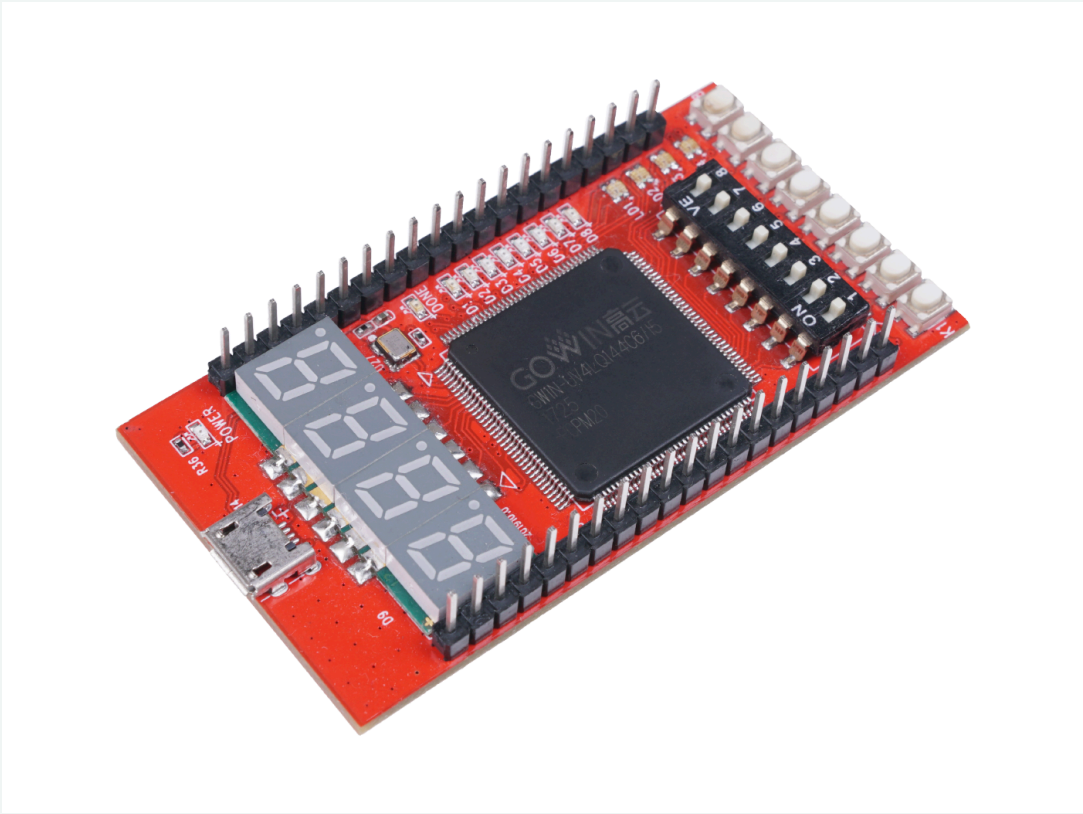
# Gowin RUNBER FPGA Development Board

This is a brand-new FPGA Development Board, which adopts the GOWIN GW1N-UV4LQ144 solution.

| SKU | UPDATED | PRICE |
| --- | --- | --- |
| 114992325 | 2026-11-11 08:32:01 | $29.90(Excl. VAT) |



# Overview



This is a brand-new FPGA Development Board, which adopts the GOWIN GW1N-UV4LQ144 solution. It is a powerful development board with the necessary resources to run FPGA applications. Also, it has a wide range of peripherals/ports for expandability. A USB-to-JTAG module is integrated on the Runber board. With the CH552T as a connector, a USB-to-micro cable can connect the onboard JTAG module via the onboard J14. The board can be powered with the VBUS from the USB or the 5V power from the expansion port or 3.3V power from an LDO. As we select the UV series devices, up to 3.3V core voltage is supported, which simplifies the power system of the board. The Runber board features an oscillator that provides the clock for CH552T and GW1N-4 at the same time at a frequency of 12MHz.

# Key Features

01 FPGA Development Board based on GOWIN GW1N-UV4LQ144

02 Wide range of ports/peripherals for expandability

03 Easily connect to PC with the help of USB-to-JTAG module

04 Can run on low voltage (3.3V)

05 Built-in oscillator with 12MHz frequency

06 Numeric display for status display

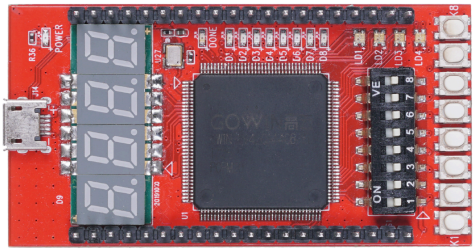
07 Eight monochrome LEDs for IO control and power indication

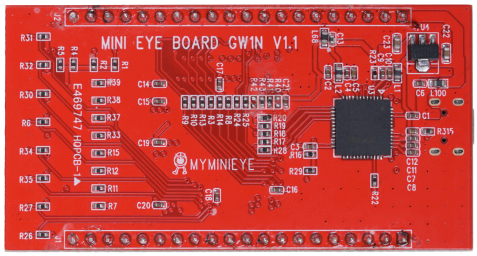
08 RGB LEDs for IO control

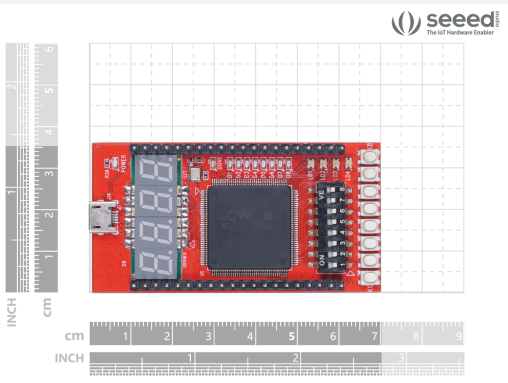
09 Integrated DIP switch and soft-touch keys for status control

10 2 set of 2.54mm pin headers (20 pins) for expandability

# Product Gallery






# Resource Links

- https://www.seeedstudio.com/Gowin-RUNBER-Development-Board-p-4779.html
- Hardware Instructions
- Instructions for Experiments
- Software User Guide
- Source Code
- Schematics

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