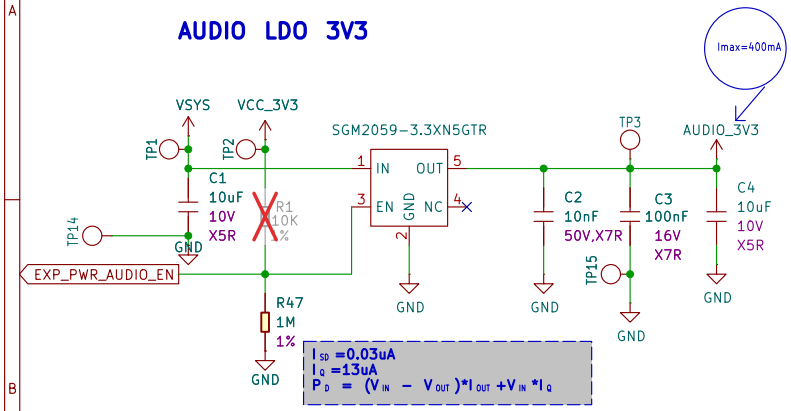
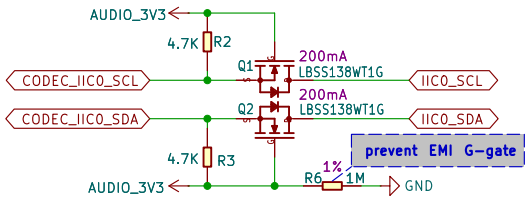
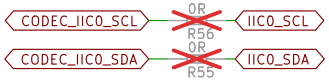
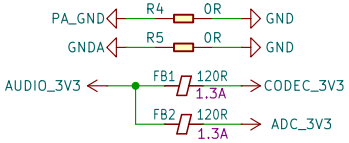
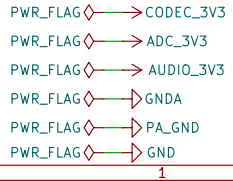
## AUDIO LDO 3V3



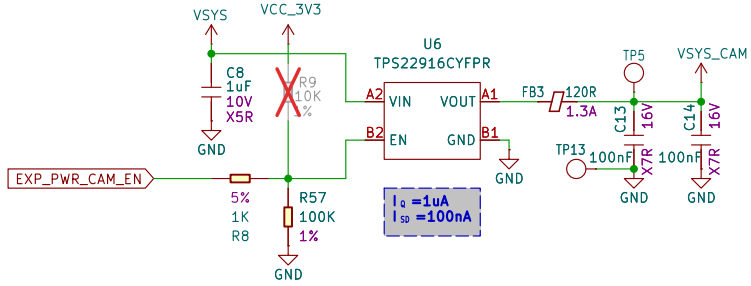



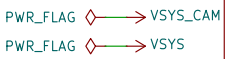



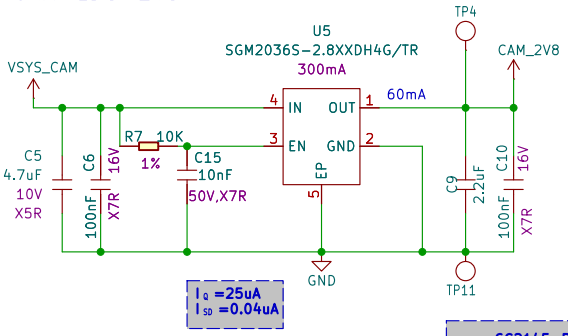




## CAM Load switch

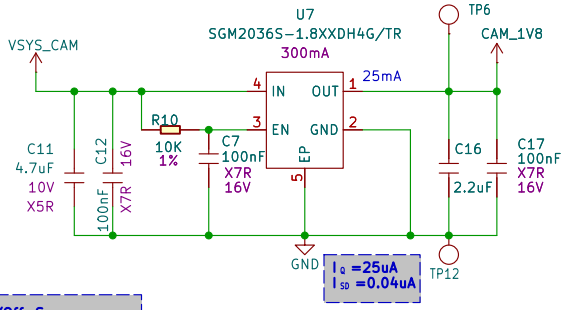


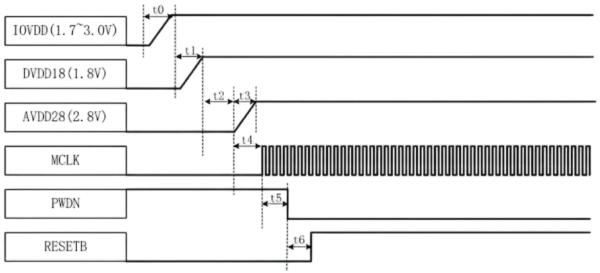

## CAM LDO 2V8




## CAM LDO 1V8




GC2145 Power On/Off Sequence




| Parameter | Description | Min. | Max. | Unit |
|---|---|---|---|---|
| t0 | IOVDD rising time | 50 | | us |
| t1 | From IOVDD to DVDD | 0 | | us |
| t2 | From DVDD to AVDD | 50 | | us |
| t3 | AVDD rising time | 50 | | us |
| t4 | From AVDD to MCLK applied | 0 | | us |
| t5 | From MCLK applied to Sensor enable | 0 | | us |
| t6 | From PWDN pull low to RESET pull high | 0 | | us |

seeed studio

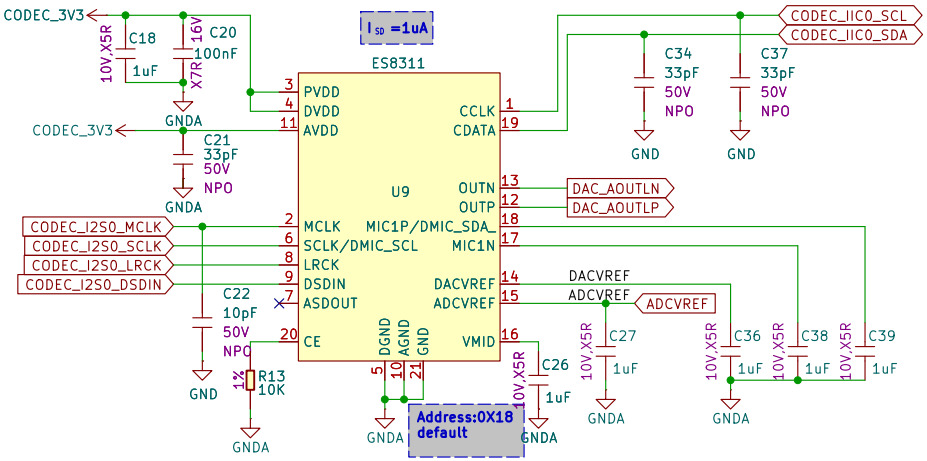
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File: POWER.kicad_sch

**Title: Wio-LR2021 Dev Kit Media HAT**

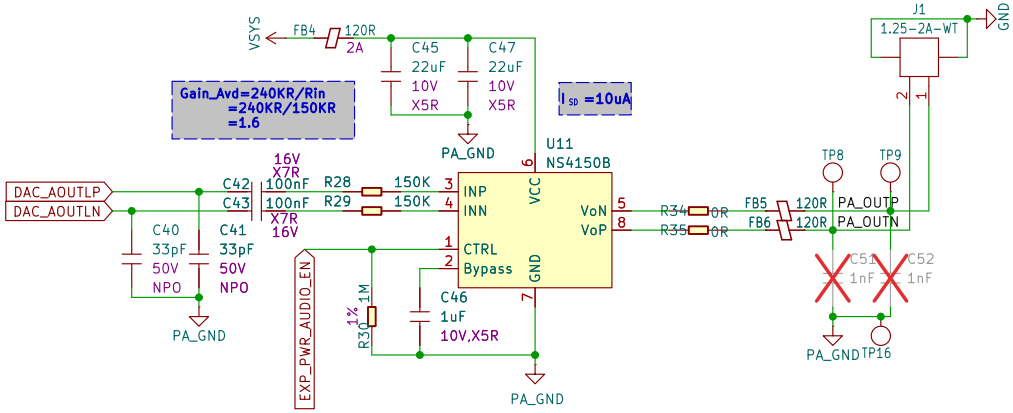
Size: A4 | Date: 2026-09-24 | Rev:
KiCad E.D.A. 10.0.2 | Id: 4/6

CC BY-SA 4.0

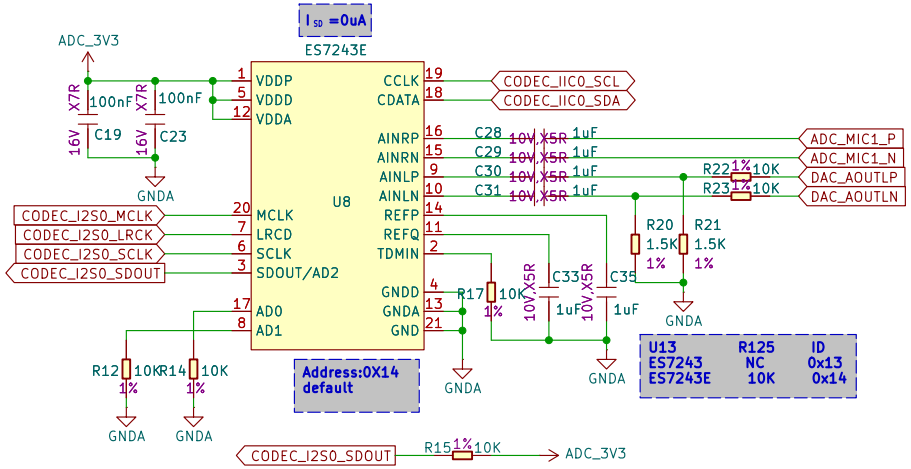
## CODEC



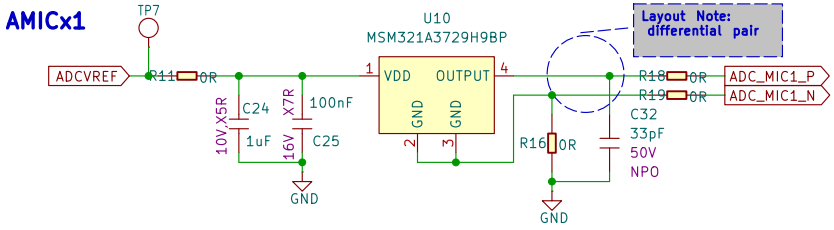

## PA & SPEAKER




## ADC




## AMICx1




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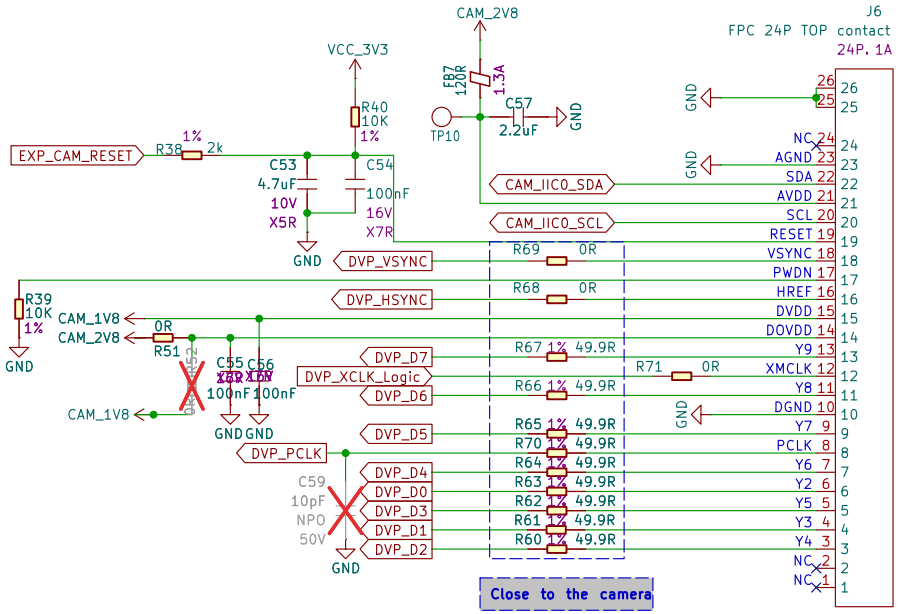
seeed studio

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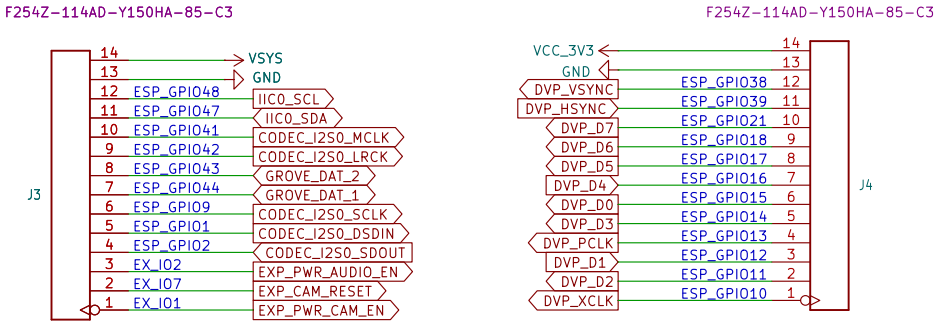
**Title: Wio-LR2021 Dev Kit Media HAT**

Size: A4 | Date: 2026-09-24 | Rev:
KiCad E.D.A. 10.0.2 | Id: 5/6

## CAM



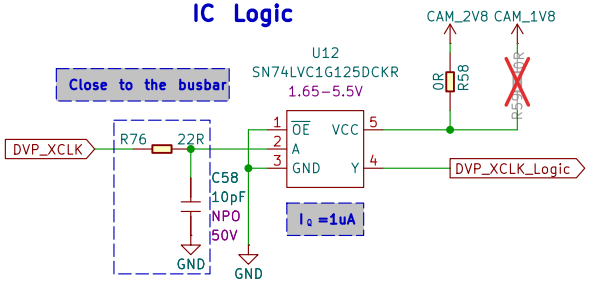

## Header

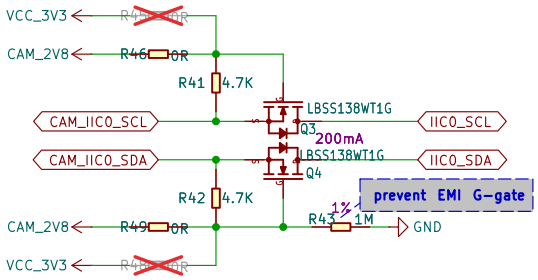



Standby Current
I AVDD =40uA
I DVDD =100uA
I IOVDD =60uA

Full size, 20Hz
I AVDD =50mA
I DVDD =70mA
I IOVDD =40mA

## IC Logic







PWR_FLAG ◇—▷ VCC_3V3

seeed studio

Sheet: /CAM & EXP HEADER/
File: CAM & EXP HEADER.kicad_sch

**Title: Wio-LR2021 Dev Kit Media HAT**

| Size: A4 | Date: 2026-09-24 | Rev: |
|---|---|---|
| KiCad E.D.A. 10.0.2 | | Id: 6/6 |

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