

	1	2	3	4	5	6	7	8
A								
B								
C								
D								
E								
F	1	2	3	4	5	6	7	8

01 Descriptions



File: 01 Descriptions.kicad_sch

02 Block Diagram



File: 02 Block Diagram.kicad_sch

Power



File: Power.kicad_sch

XIAO



File: XIAO.kicad_sch

E-ink



File: E-ink_13.3.kicad_sch



Sheet: / File: XIAO ePaper Display Board EE02_V1.0.kicad_sch			
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Size: A3	Date: 2025-11-13	Design:	Rev: v1.0
KiCad E.D.A. 9.0.3	Id: 1/6		

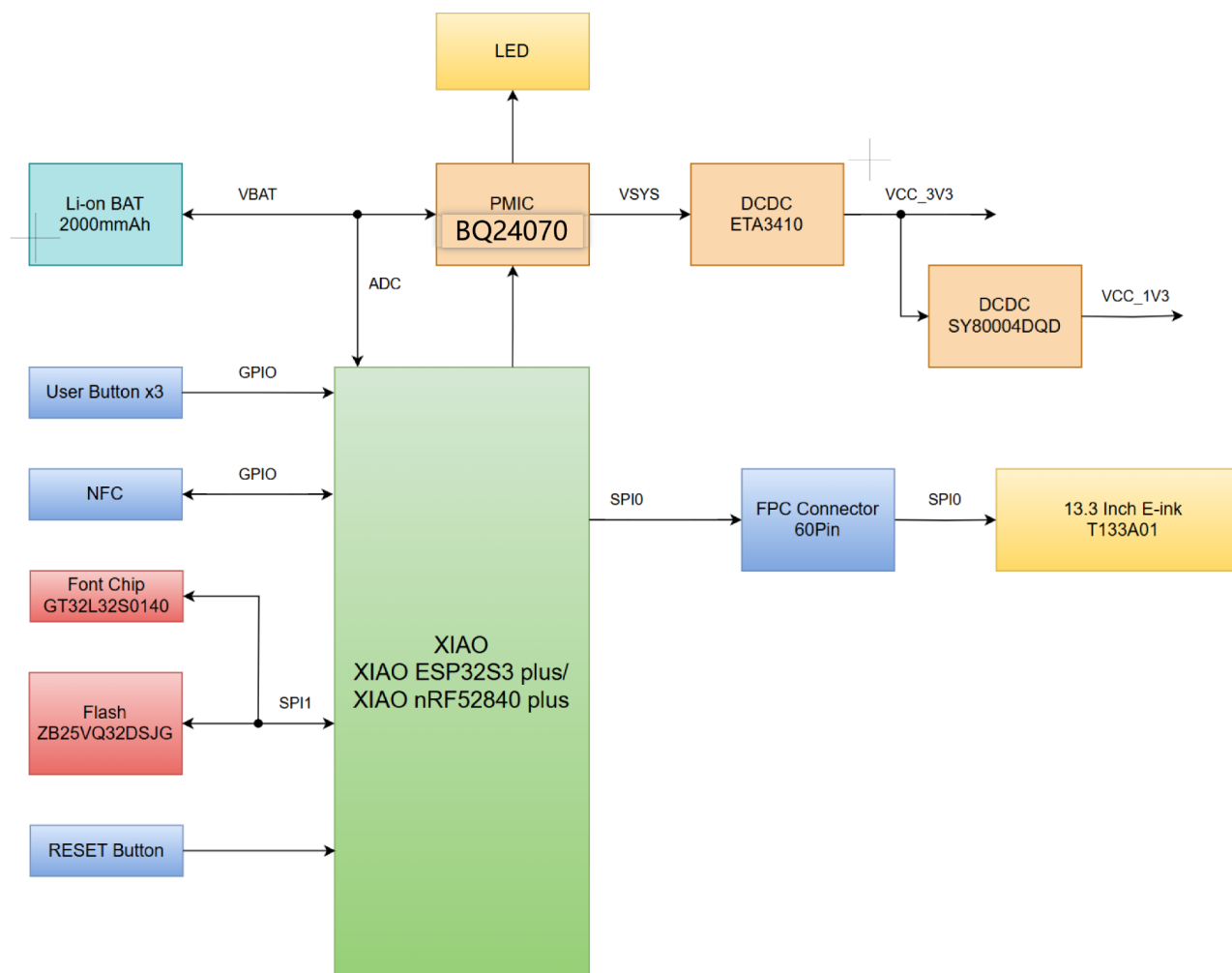
1	2	3	4	5	6	7	8
A							
B							
C							
D							
E							
F							

REV	Description	DATE	BY
V1.0	The original model number was "XIAO ePaper Display Board EE01"	20250528	Harry Qin
V1.0	The current model is "XIAO ePaper Display Board EE04". The main changes are to replace the battery connector from JST 3Pin core to JST 2Pin, and to add an NTC resistor.	20250731	Zheng Wang
V1.0	The current model is "XIAO ePaper Display Board EE02". The main changes are to Replace the original 20-pin and 50-pin ink screens with a 60-pin one that is used to connect the 13.3-inch E6 ink display.	20250805	Haoquan Pan
V1.0	This version has corrected the NTC resistor and also improved the issue where the BAT fails to switch power promptly when the VBUS is powered on or off, thus preventing voltage drops.	2025.10.19	Genta.Lee
V1.0	The PMIC has been replaced with TI's BQ24070, and the DC-DC has been changed to TI's TPS62065DSGR.	2025.11.13	Genta.Lee

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Sheet: /01 Descriptions/
File: 01 Descriptions.kicad_sch

Title: XIAO ePaper Dsiplay Board EE02			CC BY-SA 4.0
Size: A3	Date: 2025-10-22	Design:	Rev: v1.2
KiCad E.D.A. 9.0.3			Id: 2/6



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Sheet: /02 Block Diagram/
File: 02 Block Diagram.kicad_sch

Title: XIAO ePaper Display Board EE02

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Size: A4

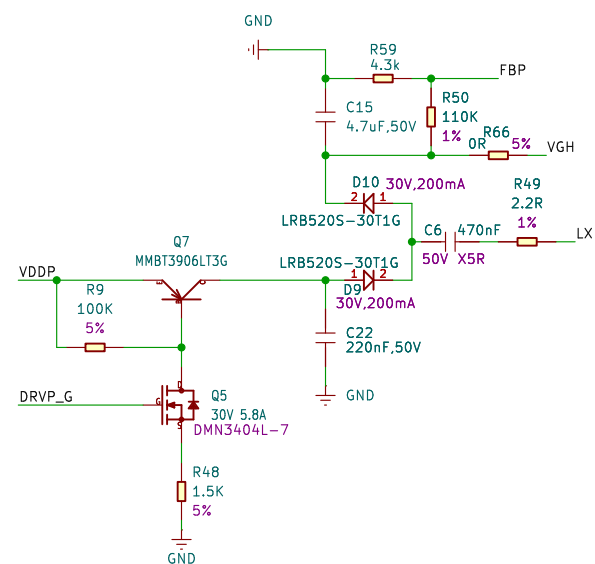
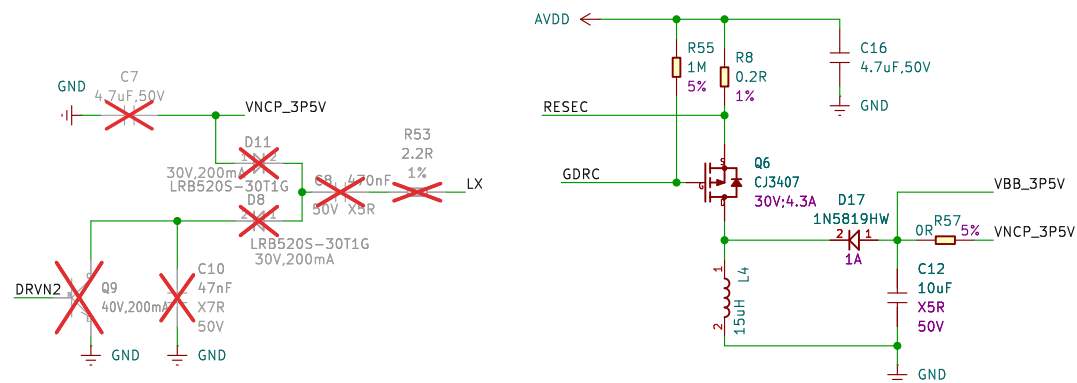
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Design:

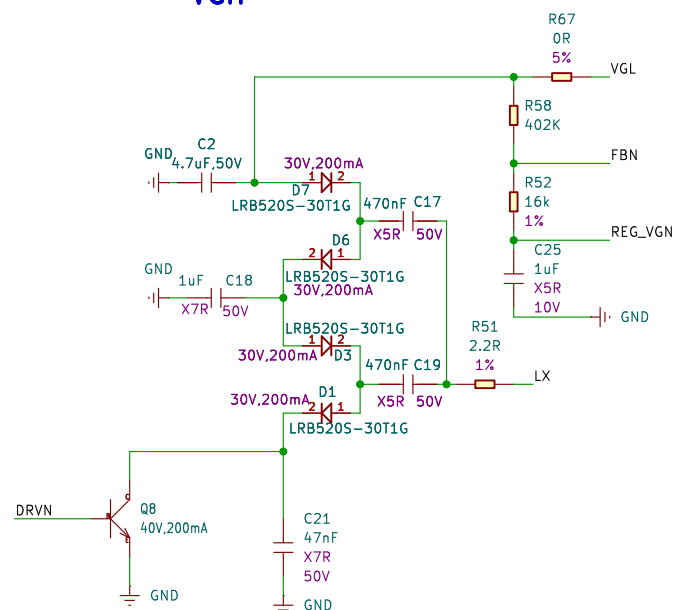
Rev: v1.0

KiCad E.D.A. 9.0.3

Id: 3/6



VGH



VGL

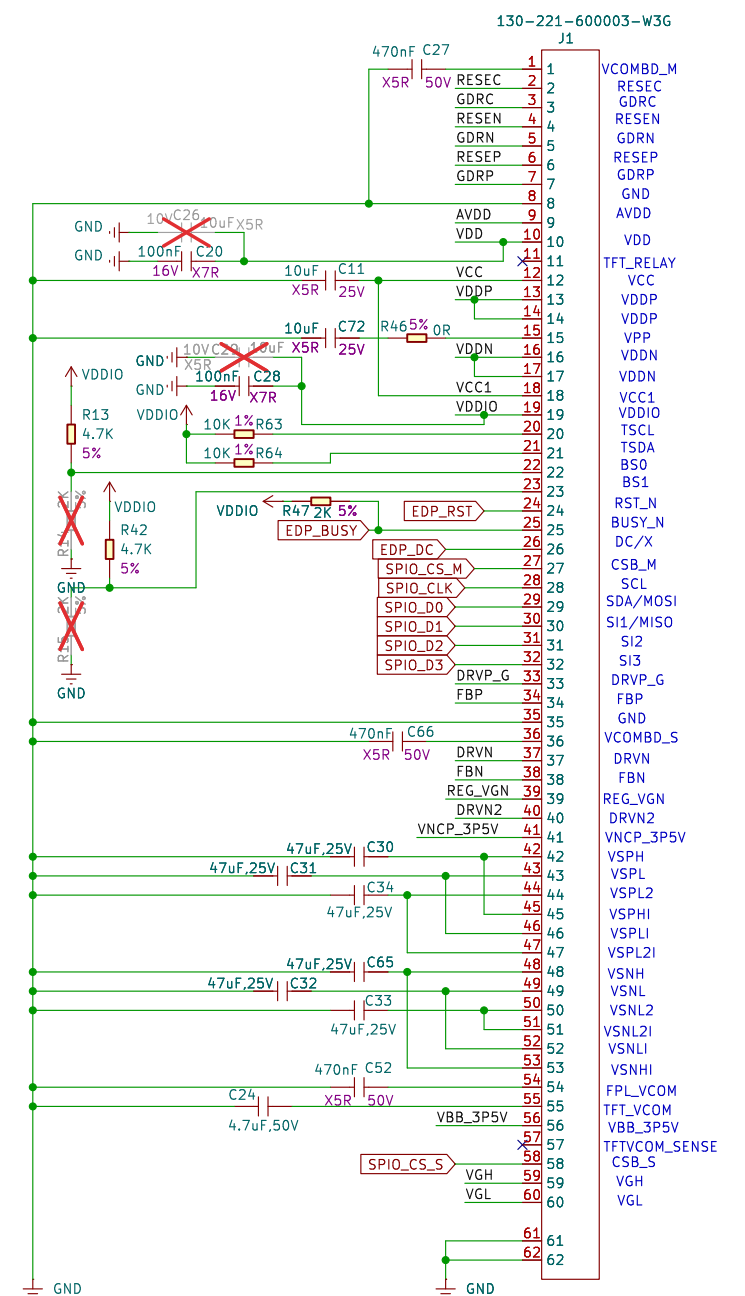
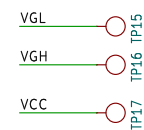
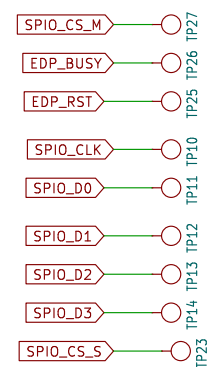


Diagram showing the connection of the SPI module to the ADXL345 sensor. The SPI module pins are connected to the ADXL345 pins as follows:

- SPI0_CS_M to TP27
- EDP_BUSY to TP26
- EDP_RST to TP25
- SPI0_CLK to TP40
- SPI0_D0 to TP41
- SPI0_D1 to TP42
- SPI0_D2 to TP43
- SPI0_D3 to TP44
- SPI0_CS_S to TP23