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REV	Description	DATE	BY
v1.0	Initial Version	2023/1/4	Linus.Liao
v1.1	Optimize the distance from the positioning hole of the shield to the pads on both sides	2023/3/27	Linus.Liao

Seeed Studio

TITLE: XIAO ESP32S3\_v1.1

Design:

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The block diagram illustrates the system architecture. Power is supplied via USB (VBUS/5V, USB D+, USB D-) to a Li Battery and a DCDC/3V3 converter. The DCDC/3V3 converter powers the ESP32 S3 and the Expansion Header. The Expansion Header connects to the XIAO Header, which provides connections for DVP, PDM, LCD, and CAM modules. The ESP32 S3 is connected to various I/O pins (IO0-IO9, IO10-IO29) and an RF U-FL Female connector. It also interfaces with an Internal PSRAM and an External Flash via SPI. The diagram includes pin definitions for the XIAO Header and the Expansion Board SD Card.

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The detailed PCB layout shows the placement of various components. Power management includes a USB connector, a Li Battery, and a DCDC/3V3 converter. The ESP32 S3 is centrally located with its pins connected to the Expansion Header and various I/O pins. The layout includes connections for an SD Card, Camera, and various I/O pins. The components are labeled with their part numbers and values. The layout is optimized for the XIAO ESP32S3 v1.1.

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