

1	2	3	4	5	6	7	8
A							
B							
C		01 Descriptions 文件: 01 Descriptions.kicad_sch	02 Block Diagram 文件: 02 Block Diagram.kicad_sch	03 Power 文件: 03 Power.kicad_sch			
D		04 XIAO Header 文件: 04 XIAO Header.kicad_sch	05 RP2040 文件: 05 RP2040.kicad_sch				
E							
F							



Sheet: /
File: XIAO RP2040.kicad_sch

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Title: XIAO RP2040 PLUS

Size: A3

Date: 2026-05-19

KiCad E.D.A. 10.0.0-19-g65df3ab11c

Rev: V1.0

Id: 1/6

1	2	3	4	5	6	7	8
A							
B							
C							
D							
E							
F							

Version	Descriptions	Data	Name
v1.0	Initail Version	12/05/2025	Genta.Lee

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Sheet: /01 Descriptions/
File: 01 Descriptions.kicad_sch

Title: XIAO RP2040 PLUS

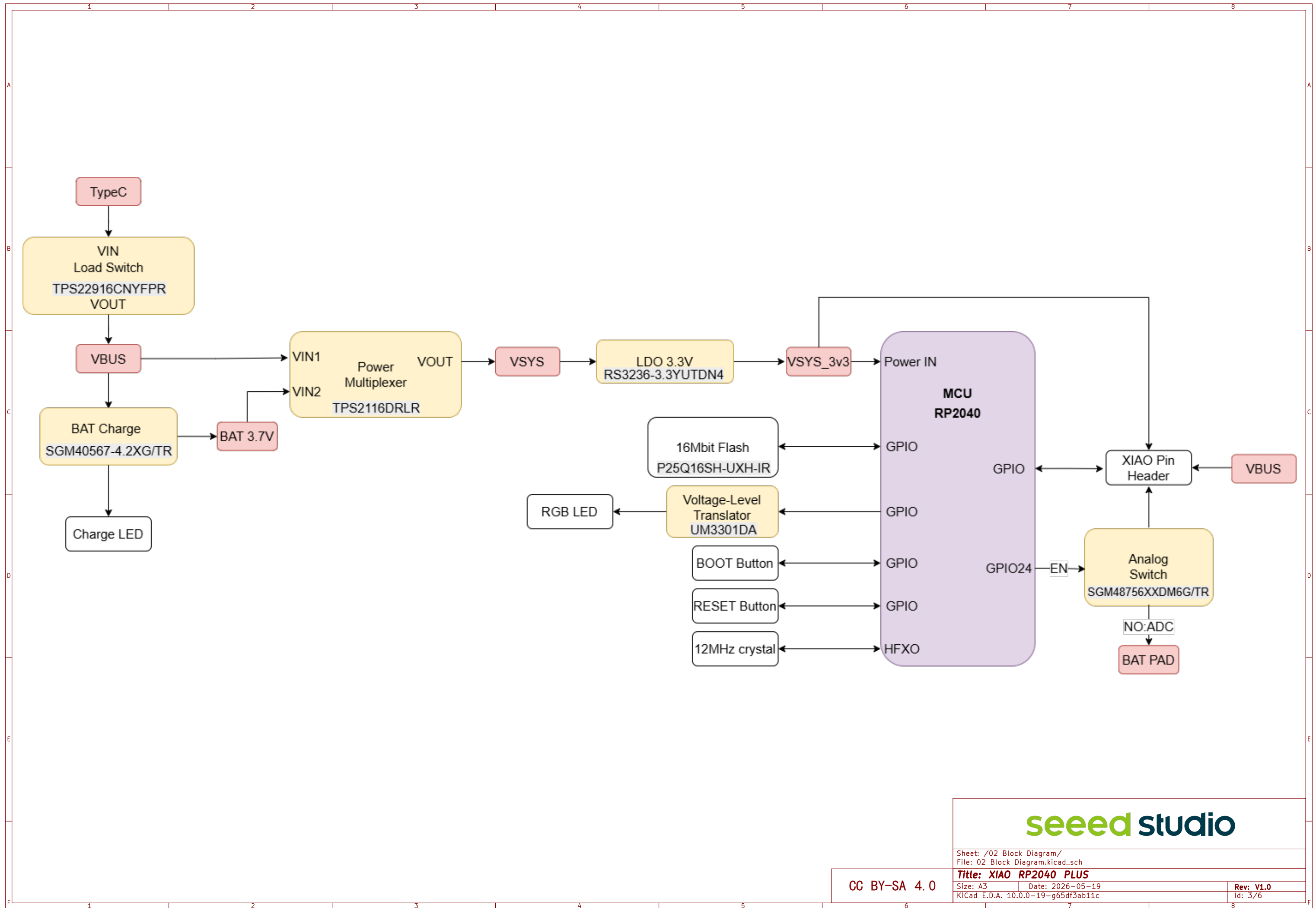
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Date: 2026-05-19

Rev: V1.0

KiCad E.D.A. 10.0.0-19-g65df3ab11c

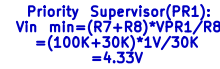
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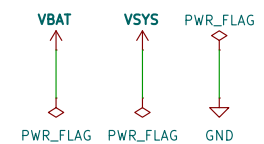
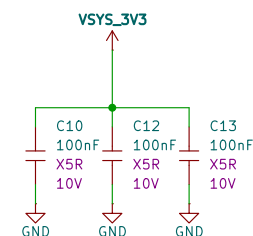
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Title: XIAO RP2040 PLUS		
Size: A3	Date: 2026-05-19	Rev: V1.0
KiCad E.D.A. 10.0.0-19-g65df3ab11c		Id: 3/6

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The diagram shows the SG M40567-4.2XG/TR IC (U1) connected to a 5V USB source (VBUS) and a 5V battery (VBAT). The input voltage (VIN) is 5V, and the output voltage (VOUT) is 4.2V. The IC is configured with a 1uF capacitor (C2) and a 120K resistor (R4) at the input. The output is connected to the battery (VBAT) through a 1uF capacitor (C3). The IC is also connected to a red LED (Charge_LED) and GND.



Sheet: /03 Power/	
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Title: XIAO RP2040 PLUS	
Size: A3	Date: 2026-05-19
KiCad E.D.A. 10.0.0-19-g65df3ab11c	Rev: V1.0 Id: 4/6

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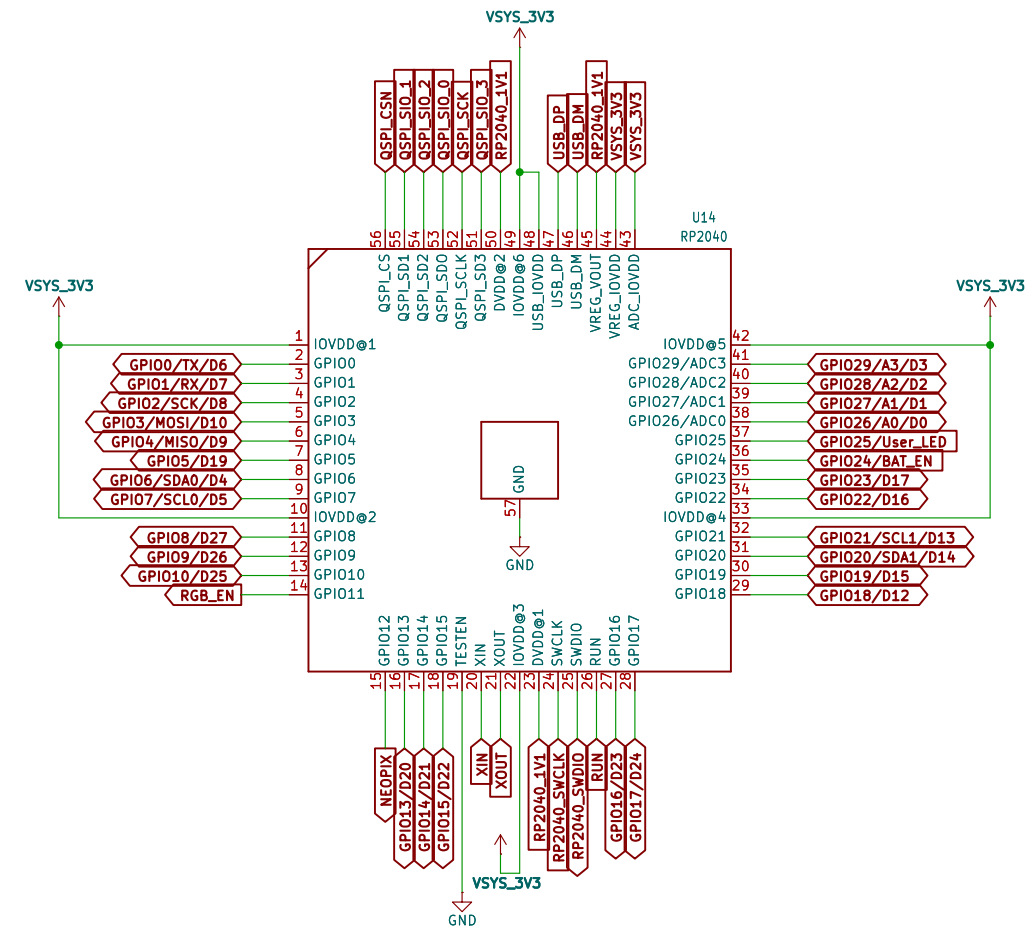
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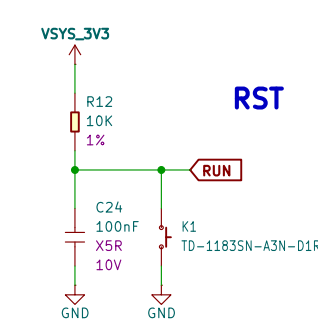
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Size: A3	Date: 2026-05-19	Rev: V1.0
KiCad E.D.A. 10.0.0-19-g65df3ab11c		Id: 5/6

Size: A3	Date: 2026-05-19	Rev: V1.0
KiCad E.D.A. 10.0.0-19-g65df3ab11c		Id: 5/6

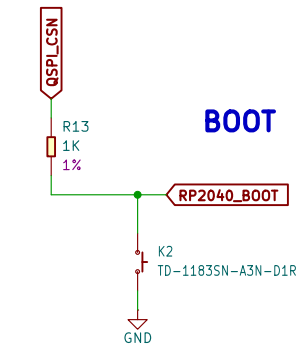
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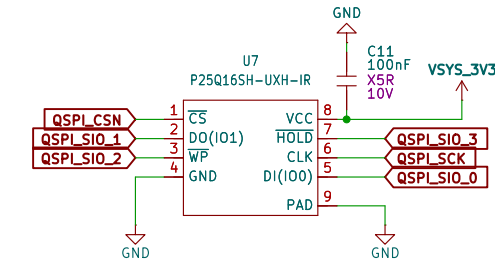
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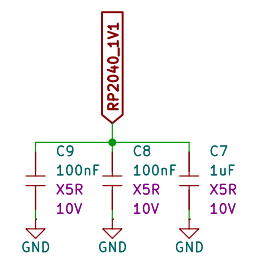
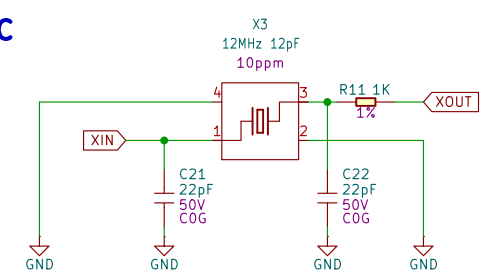
BOOT



FLASH



OSC



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Sheet: /05 RP2040/ File: 05 RP2040.kicad_sch		
Title: XIAO RP2040 PLUS		
Size: A3	Date: 2026-05-19	Rev: V1.0
KiCad E.D.A. 10.0.0-19-g65df3ab11c		Id: 6/6

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