

1	2	3	4	5	6
A	01 Descriptions	02 Block Diagram	03 Power		A
B	File: 01 Descriptions.kicad_sch	File: 02 Block Diagram.kicad_sch	File: 03 Power.kicad_sch		B
C	04 Debug & XIAO Header	05 nRF54L15-CAAA-R	06 Peripherals		C
D	File: 04 Debug & XIAO Header.kicad_sch	File: 05 nRF54L15-CAAA-R.kicad_sch	File: 06 Peripherals.kicad_sch		D
1	2	3	4	5	6



Sheet: /

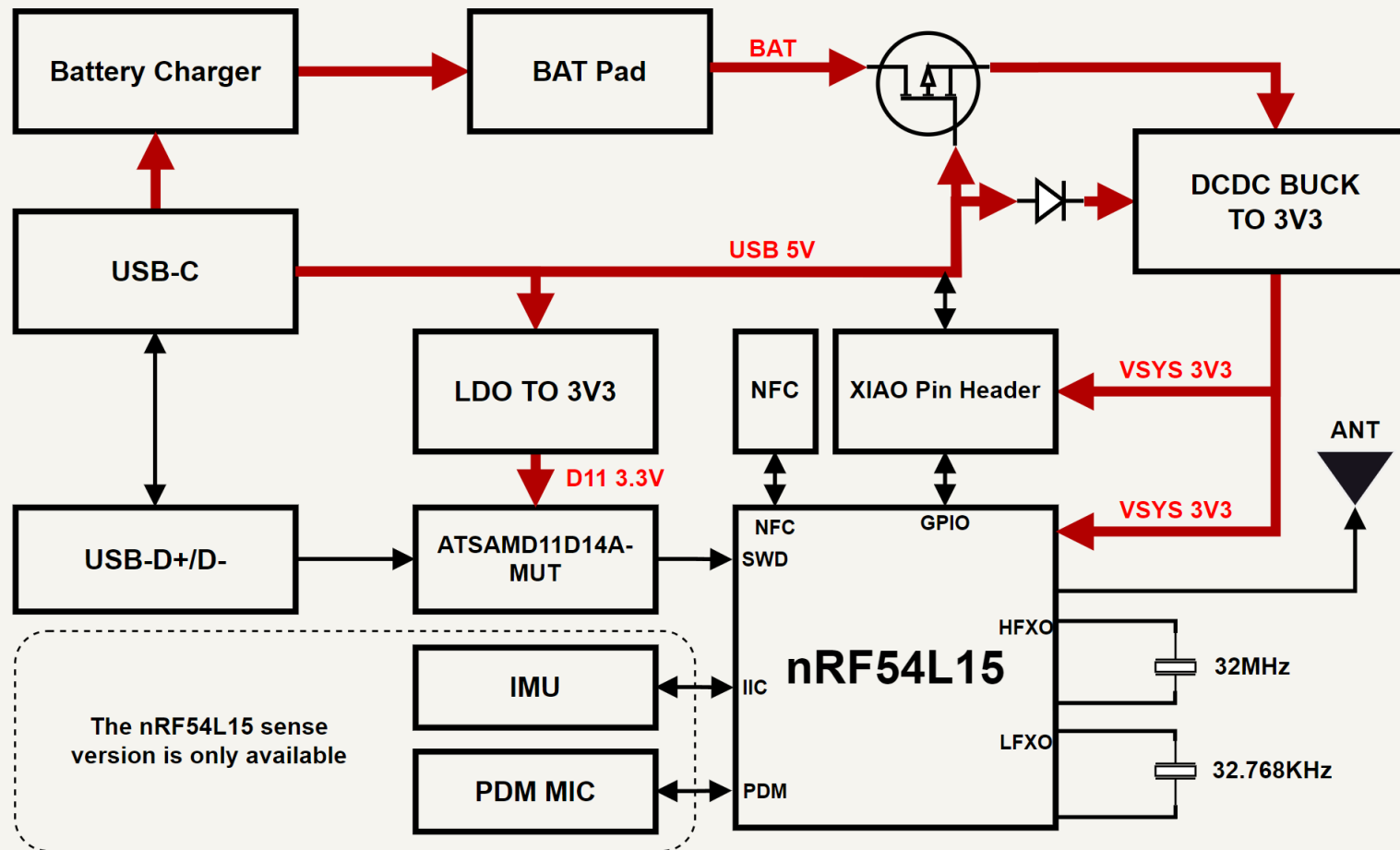
File: XIAO nRF54l15.kicad\_sch

Title: XIAO nRF54L15			CC BY-SA 4.0
Size: A4	Date: 2025-05-29	Design: Wenkang.Liu	Rev: V0.9
KiCad E.D.A. 9.0.0			Id: 1/7

Version	Descriptions	Data	Name
v1.0	First Release	4/16/2024	wenkang.liu

seeed studio

Sheet: /01 Descriptions/ File: 01 Descriptions.kicad_sch			
Title: XIAO nRF54L15			CC BY-SA 4.0
Size: A4	Date: 2025-05-29	Design: Wenkang.Liu	Rev: V0.9
KiCad E.D.A. 9.0.0			Id: 2/7



**seeed studio**

Sheet: /02 Block Diagram/  
File: 02 Block Diagram.kicad\_sch

**Title: XIAO nRF54L15**

CC BY-SA 4.0

Size: A4

Date: 2025-05-29

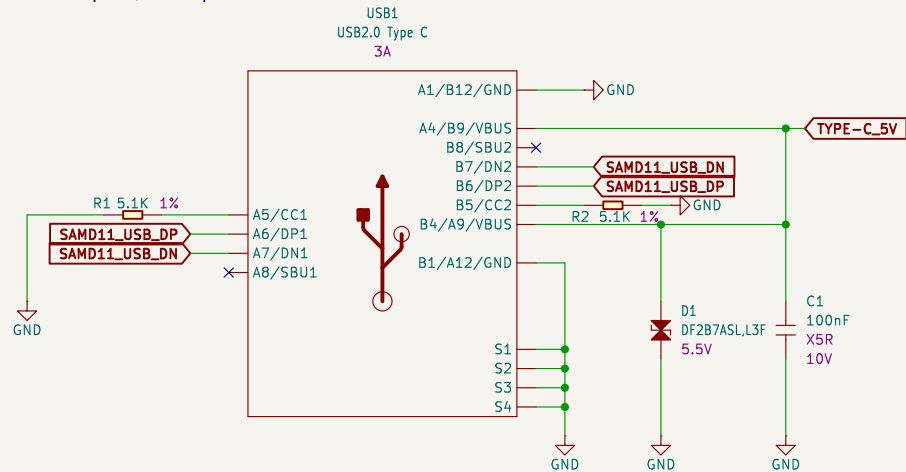
Design: Wenkang.Liu

Rev: V0.9

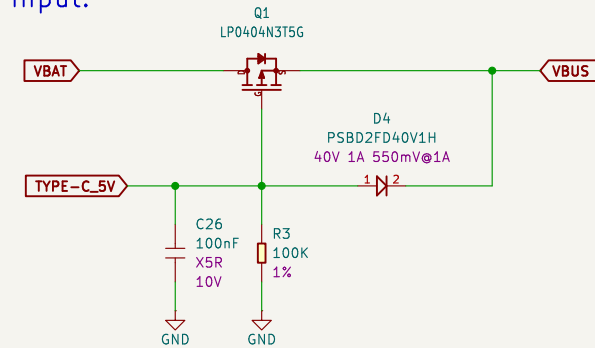
KiCad E.D.A. 9.0.0

Id: 3/7

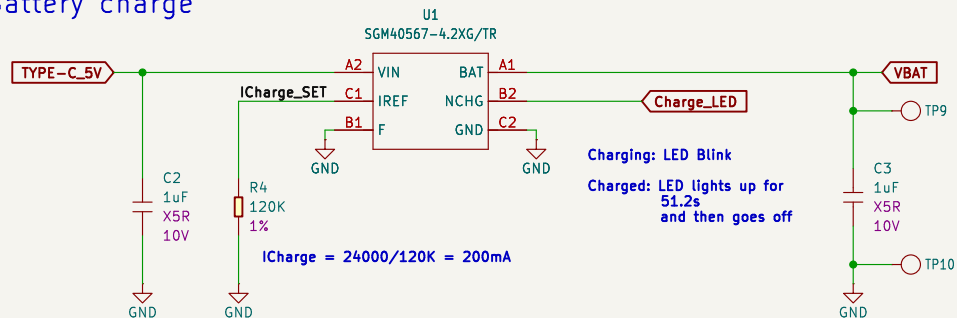
TYPE C input/output:



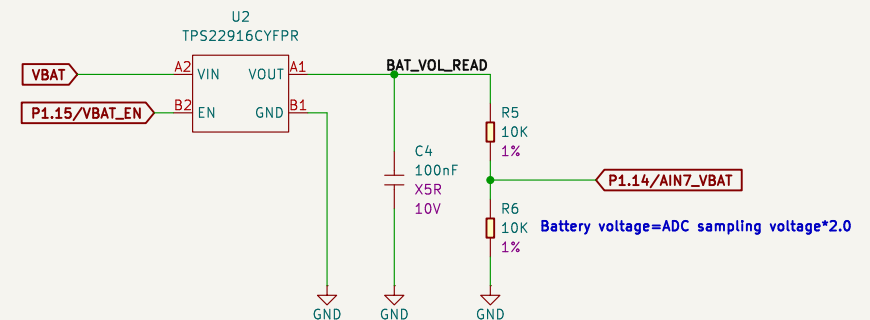
User Power input:



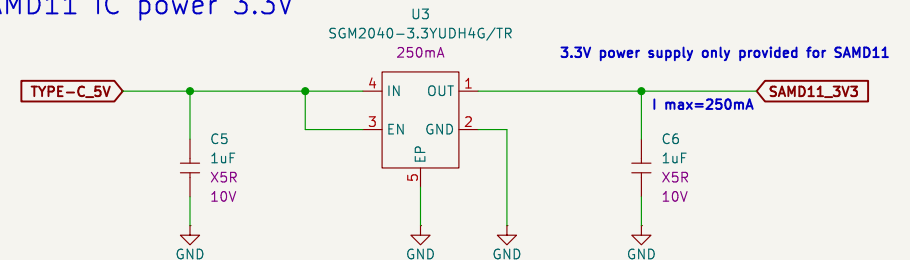
Battery charge



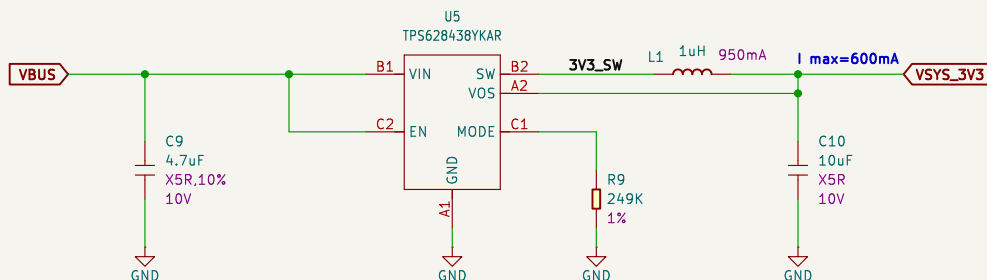
## Battery voltage detection



SAMD11 IC power 3.3V



System 3.3V



**seed studio**

Sheet: /03 Power/

File: 03 Power.kicad\_sch

Title: XIAO nRF54L15

Size: A4

Date: 2025-05-29

Design: Wenkang.Liu

Size: A4	
KiCad E.D.A. 9.0.0	

CC BY-SA 4.0

Rev: V0.9

Id: 4/7

[illegible]

This diagram illustrates the pin connections for the XIAO nRF54L15 development board, categorized into Back debugging function points, NFC Function Points, Back functional points, and XIAO Header connections.

### Back debugging function points

Pin	Function
nRF54_SWCLK	TP1 SWCLK
nRF54_SWDIO	TP2 SWDIO
GND	TP3 GND
nRF54_RESET	TP4 RST
VSYS_3V3	TP5 3V3
SAMD11_RESET	TP6 RST2
SAMD11_SWCLK	TP7 SWCLK2
SAMD11_SWDIO	TP8 SWDIO2

### NFC Function Points

Pin	Function
TP19	P1.02/NFC1 N1
TP20	P1.03/NFC2 N2

### Back functional points

Pin	Function
P1.08/SAMD11_TX	TP11
P1.09/SAMD11_RX	TP12
P0.03/D11/IMU_SCL	TP13 D11
P0.04/D12/IMU_SDA	TP14 D12
P2.00/USR_LED	TP18
P2.06/D15	TP17 D15
P2.09/D14	TP16 D14
P2.10/D13	TP15 D13

### XIAO Header

U9 XIAO Pin

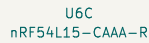
Pin	Function
1	A0_D0
2	A1_D1
3	A2_D2
4	A3_D3
5	A4_D4_SDA
6	A5_D5_SCL
7	D6_TX
8	P2.07/D7/RX
9	P2.01/D8/SCK
10	P2.04/D9/MISO
11	P2.02/D10/MOSI
12	VSYS_3V3
13	GND
14	VBUS

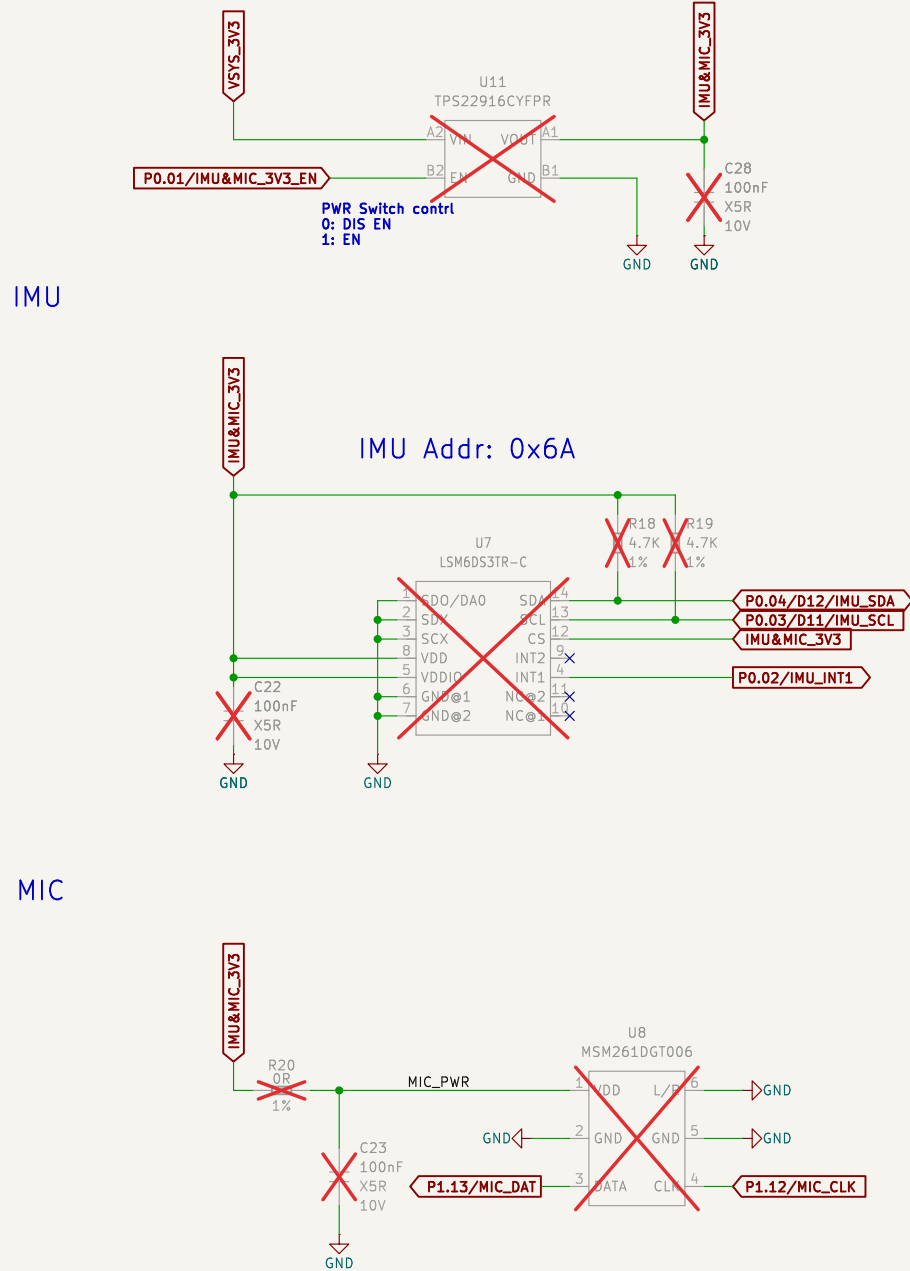
### seeed studio

Sheet: /04 Debug & XIAO Header/  
File: 04 Debug & XIAO Header.kicad\_sch

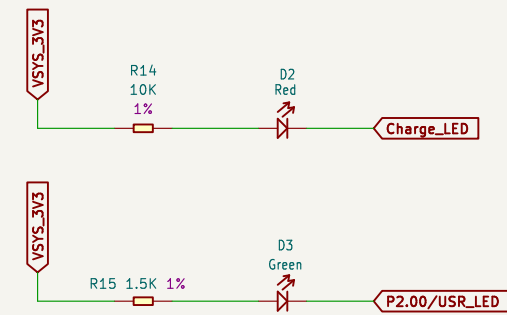
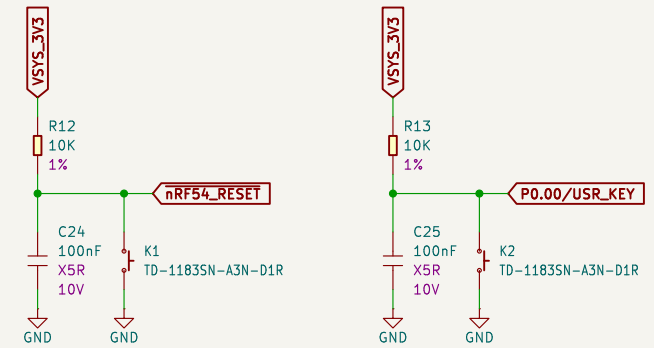
Title: XIAO nRF54L15			CC BY-SA 4.0
Size: A4	Date: 2025-05-29	Design: Wenkang.Liu	Rev: V0.9
KiCad E.D.A. 9.0.0			Id: 5/7

U6B  
nRF54L15-CAAA-R





## KEY



**seed studio**

Sheet: /06 Peripherals/  
File: 06 Peripherals.kicad\_sch

Title: XIAO nRF54L15

Title: XIAO nRF54L15

Size: A4	
KiCad E.D.A. 9.0.0	

Date: 2025-05-29

Design: Wenkang.Liu

CC BY-SA 4.0

Rev: V0.9

Id: 7/7