

P3737-A05

Jetson Orin Developer Kit Carrier Board

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	A	B	C	D	E	F	G	H



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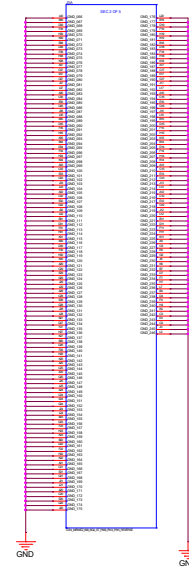
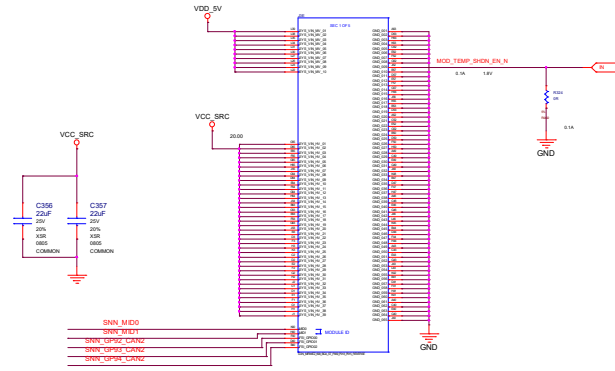
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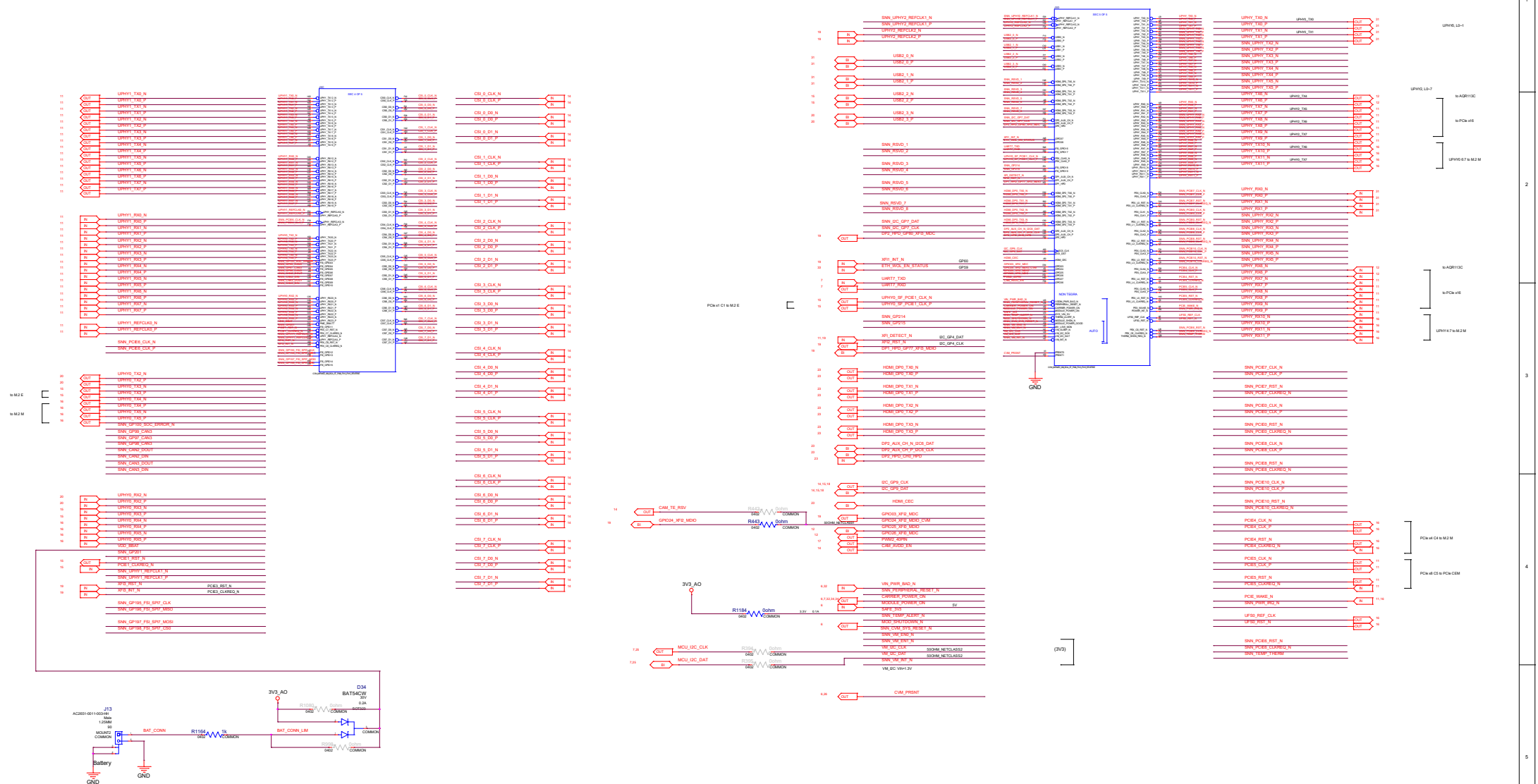
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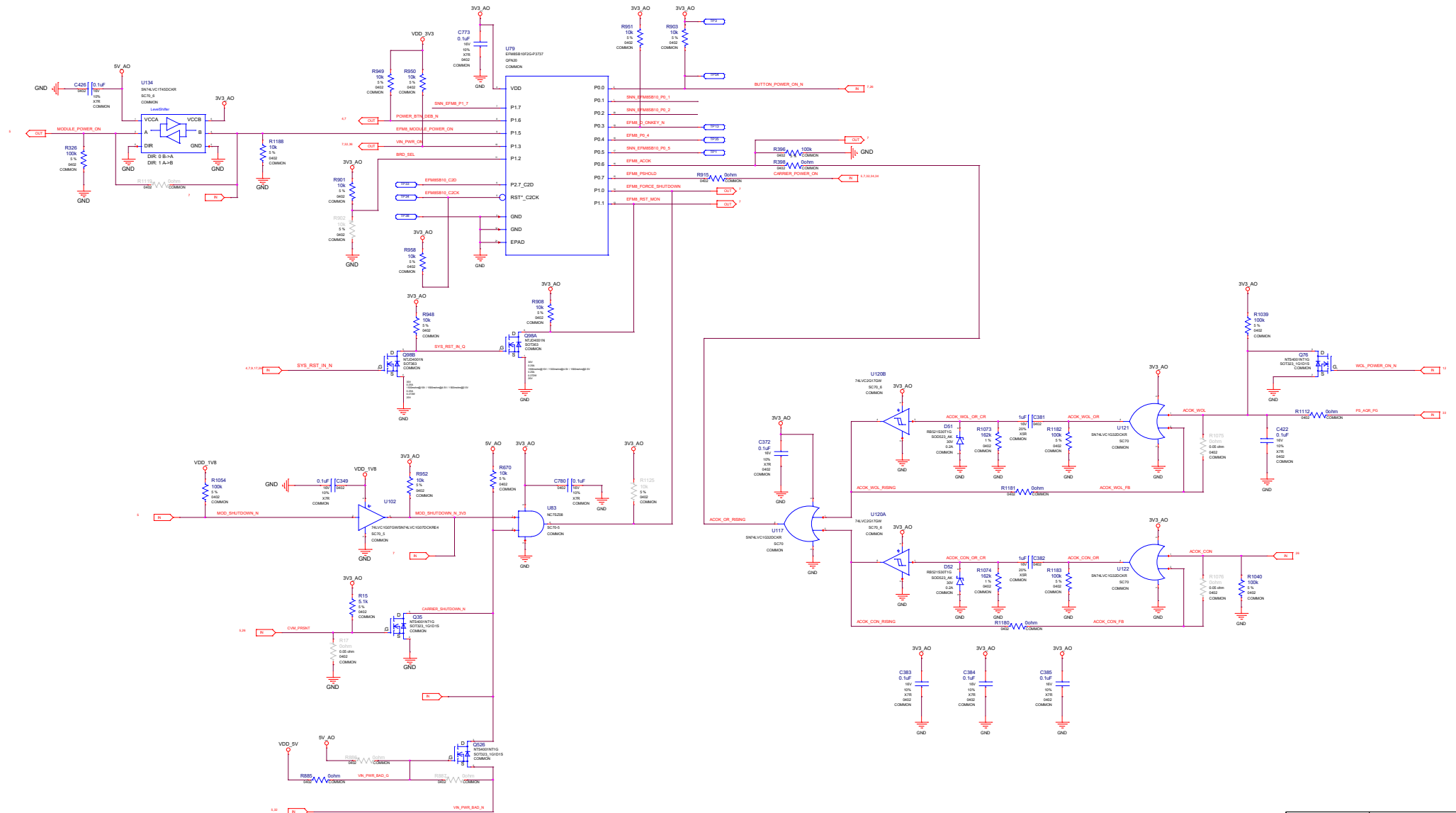
CVM CONNECTOR 1/3



CVM CONNECTOR 3/3



Button MCU



Debug MCU

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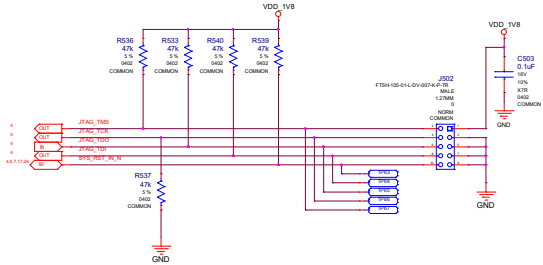
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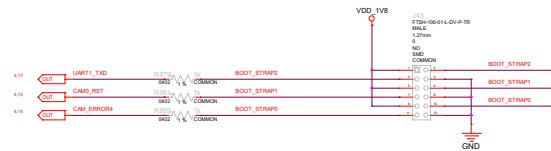
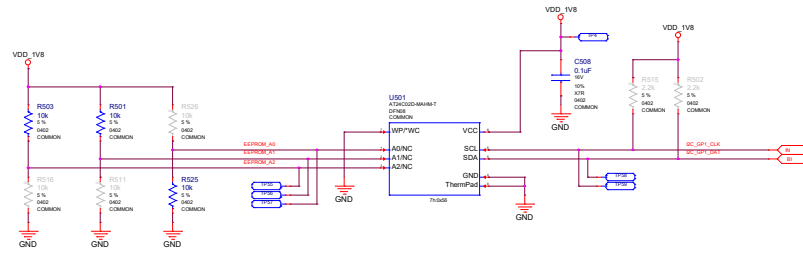
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AP DEBUG CONNECTOR



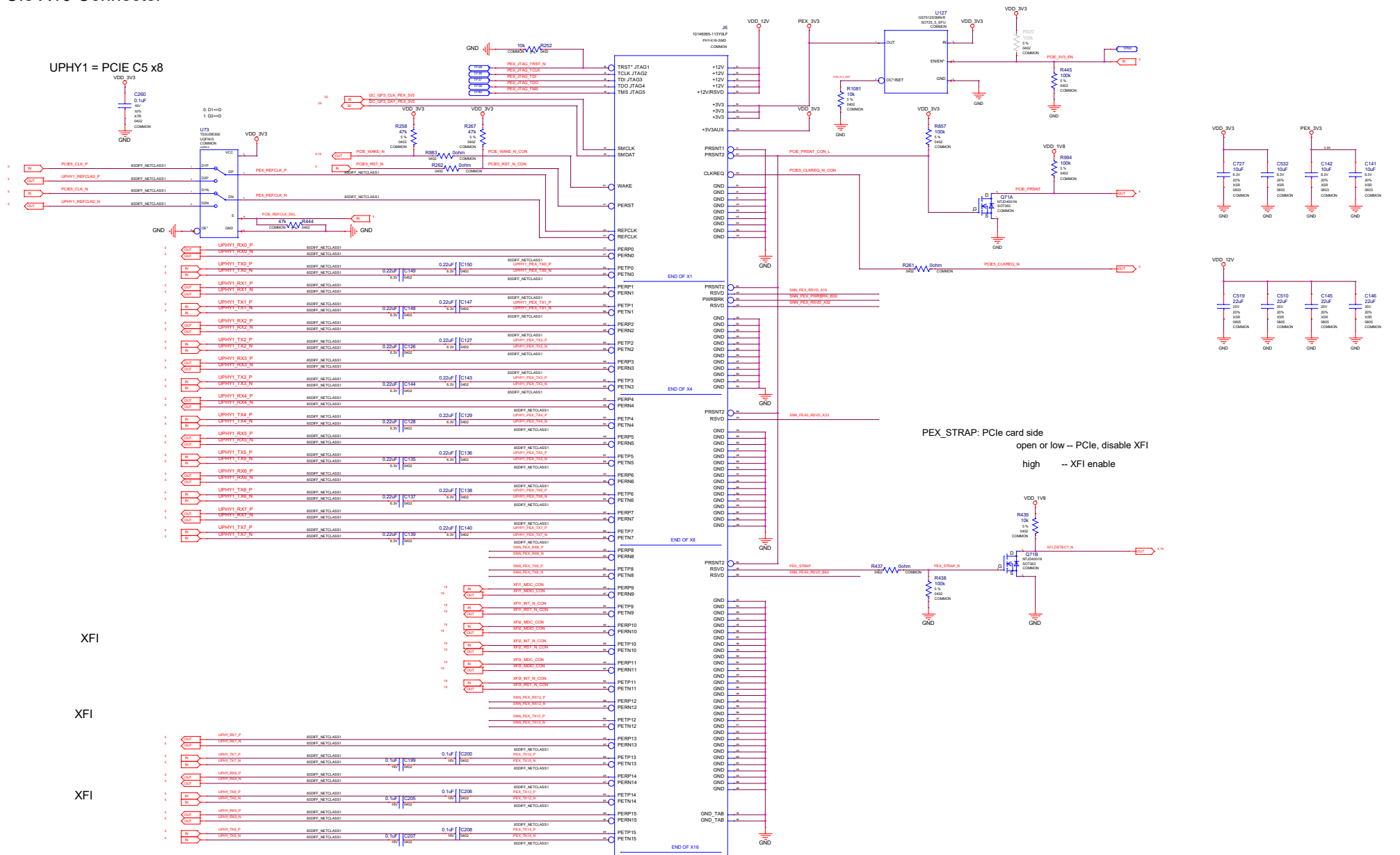
JTAG

ID EEPROM



PCIe X16 Connector

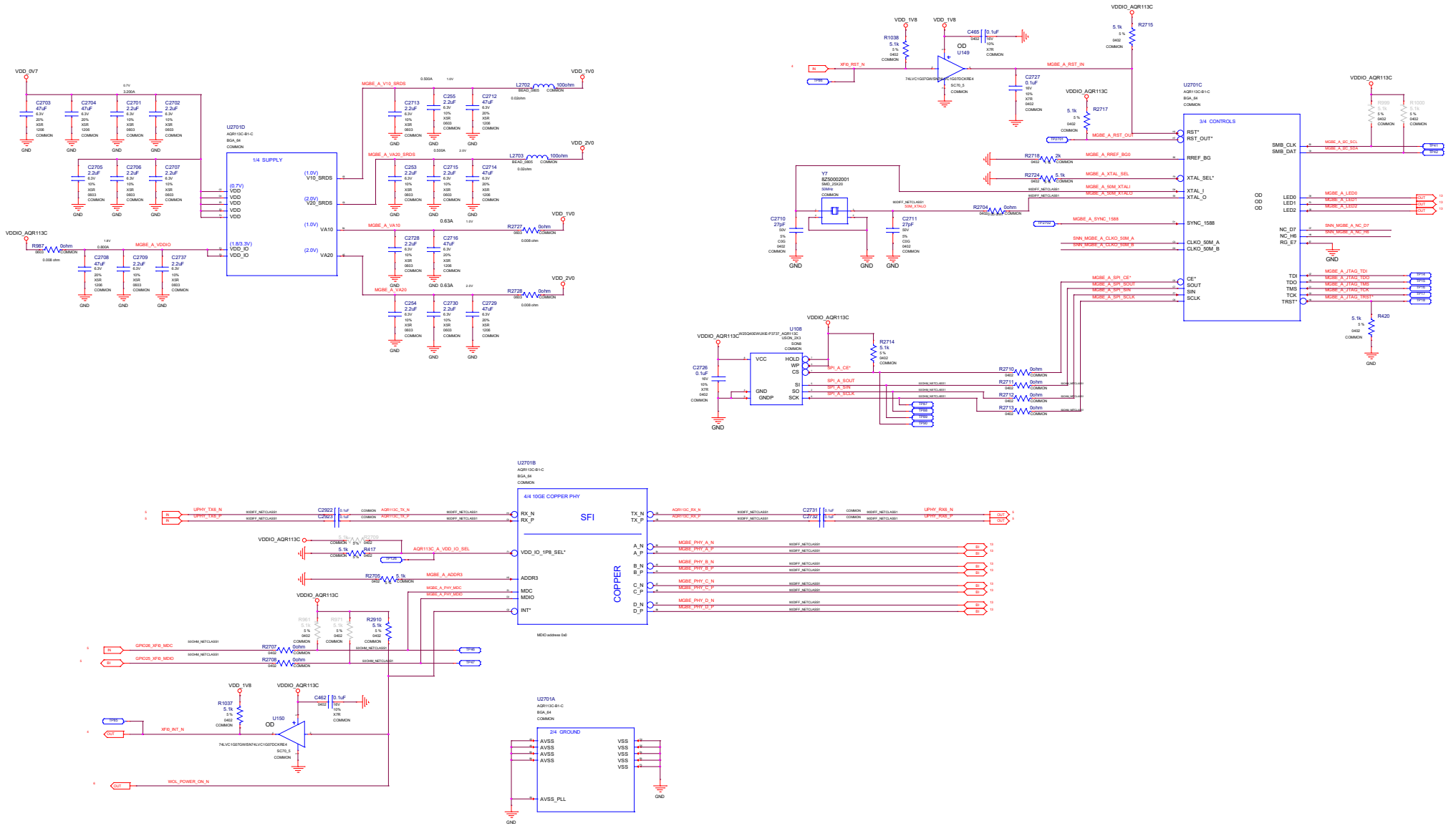
UPHY1 = PCIE C5 x8



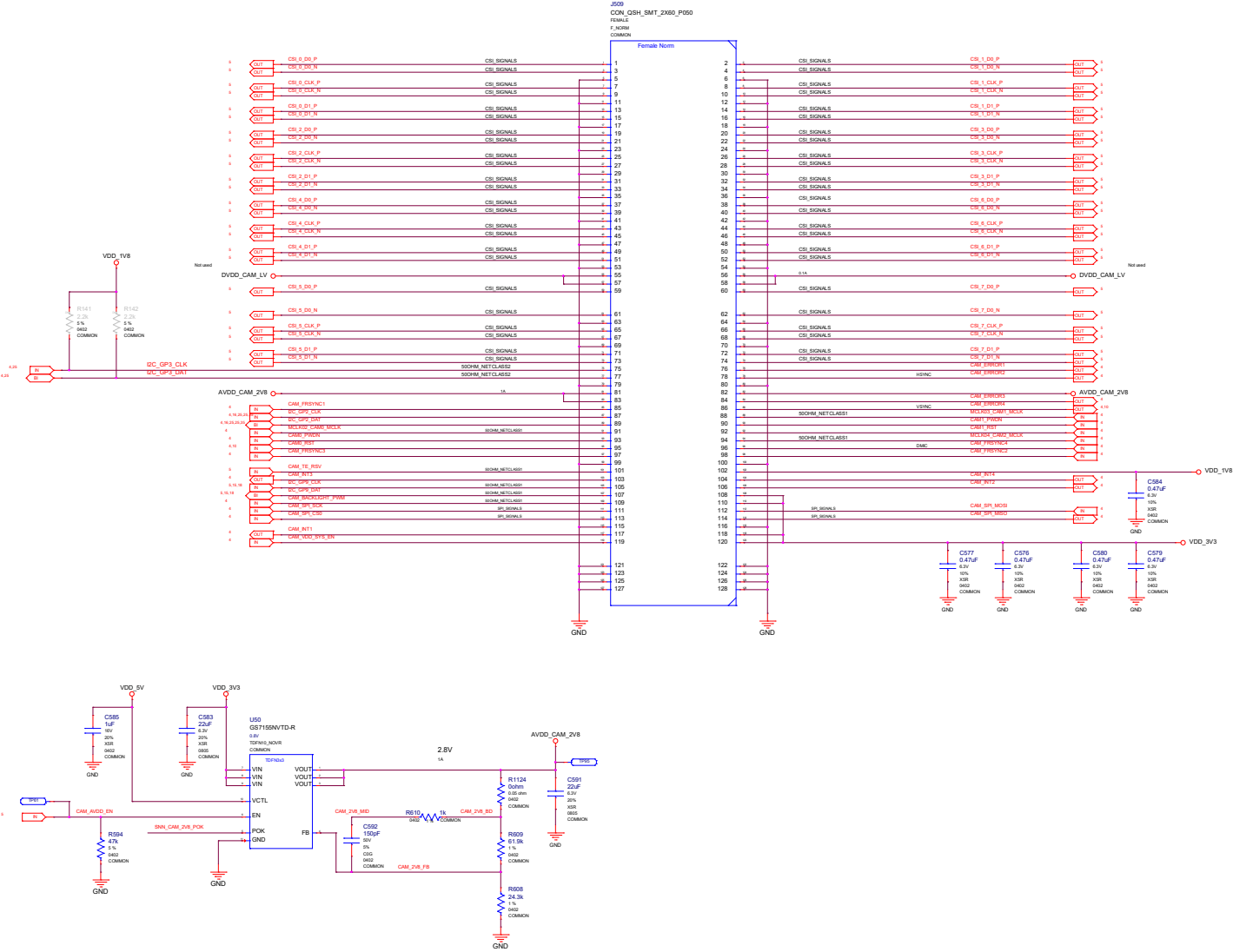
XFI connections shown on the PCIe connector are not supported

PEX_STAP: PCIe card side
open or low -- PCIe, disable XFI
high -- XFI enable

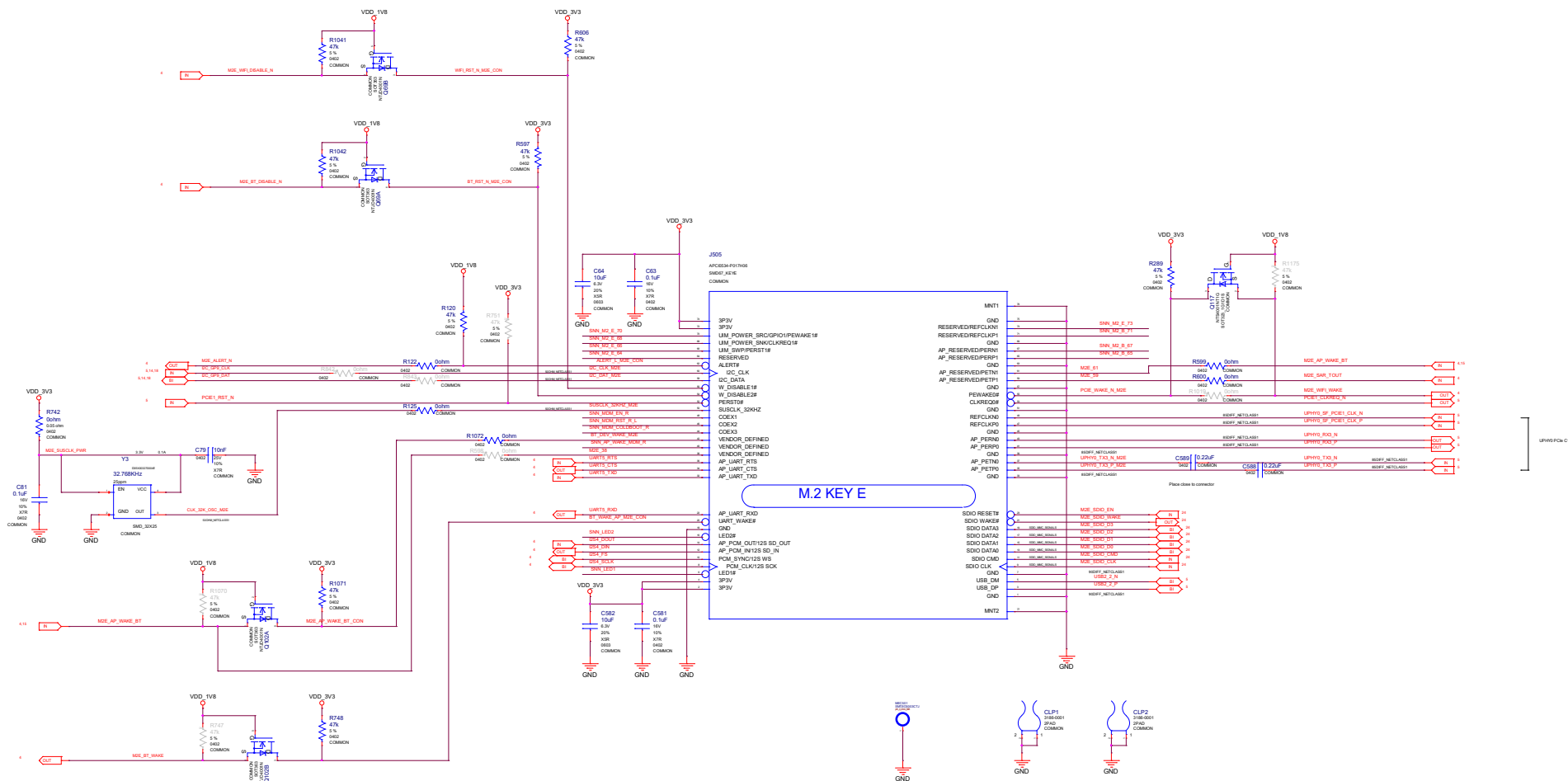
10GB ETH PHY AQR113C



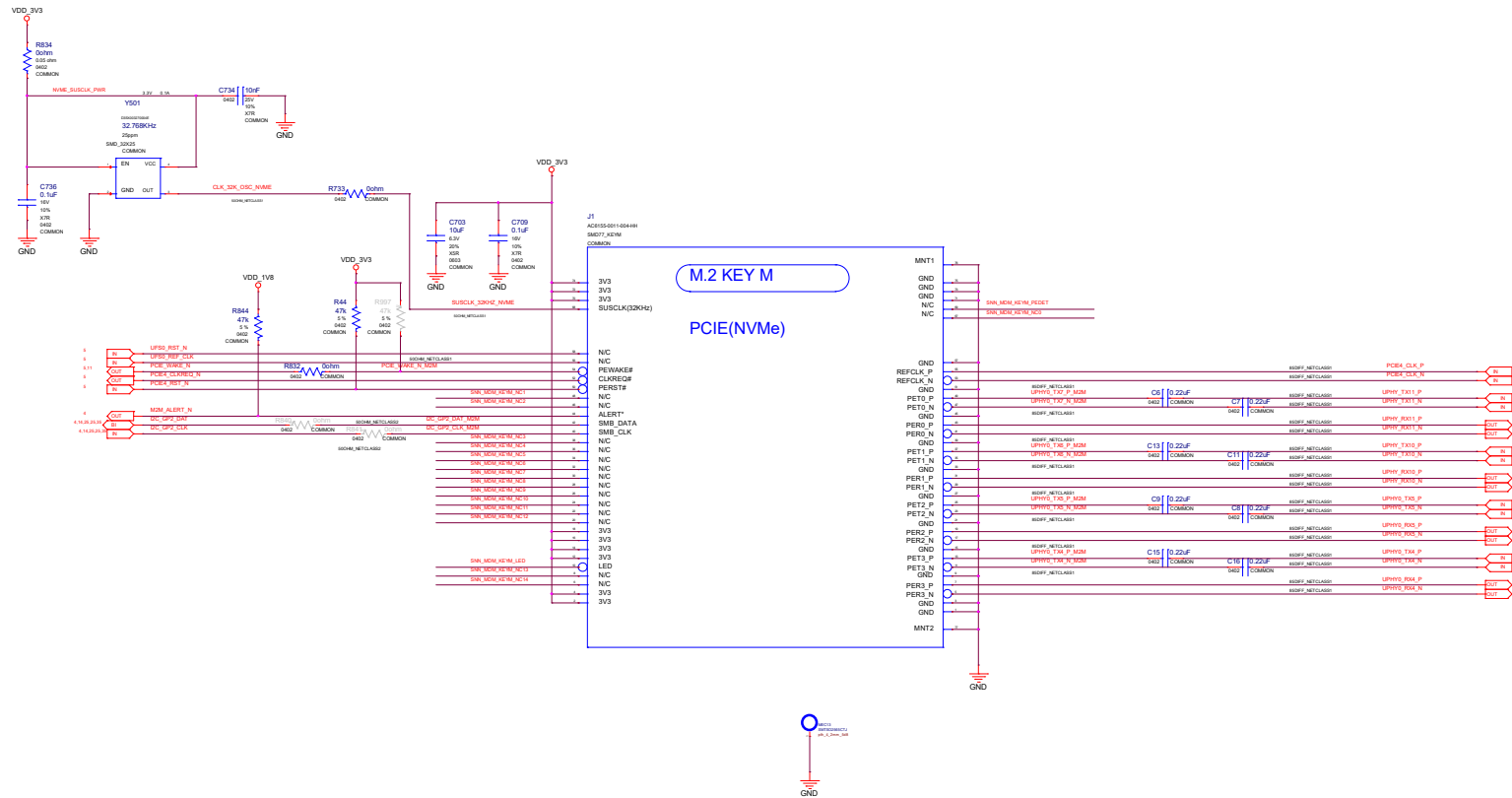
Camera Connector

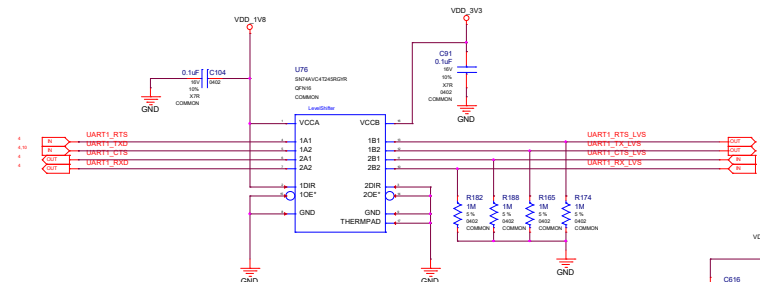
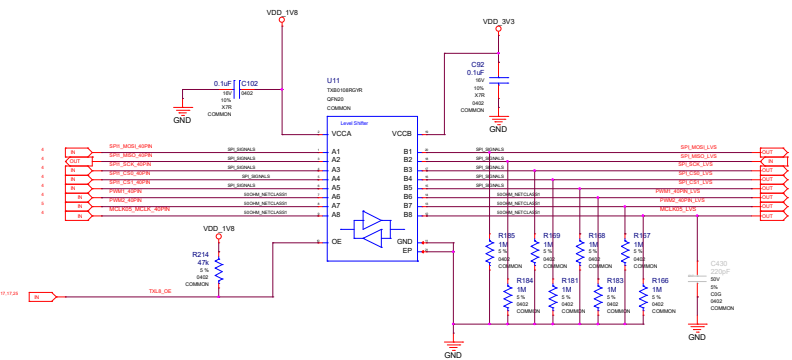
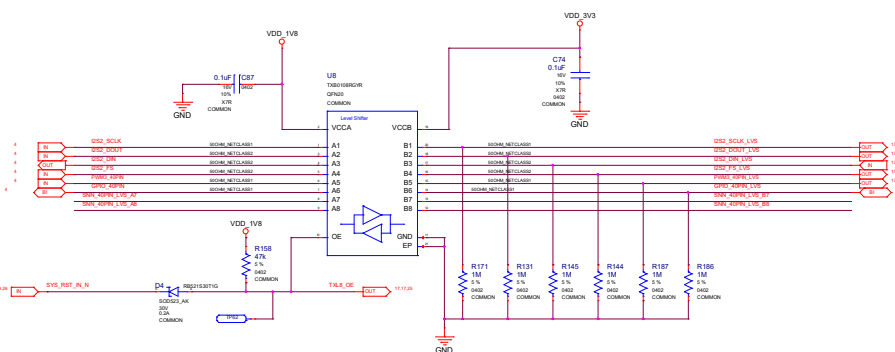
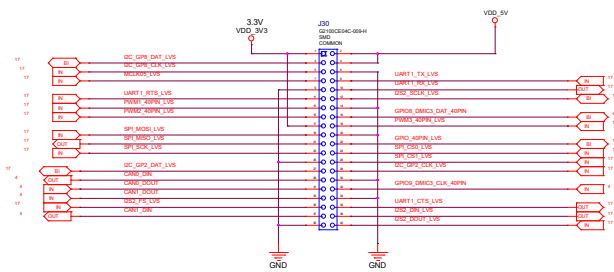


M.2 Key E Connector

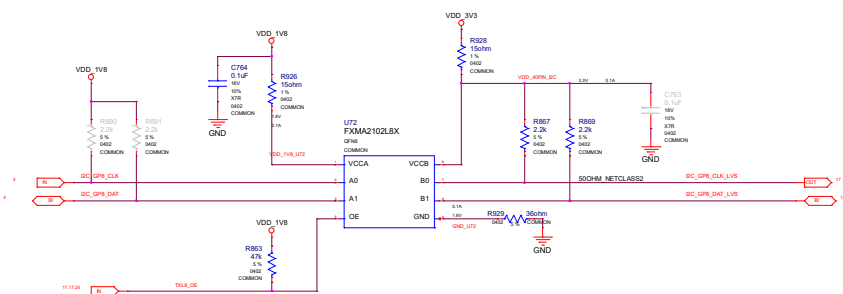


M.2 Key M Connector

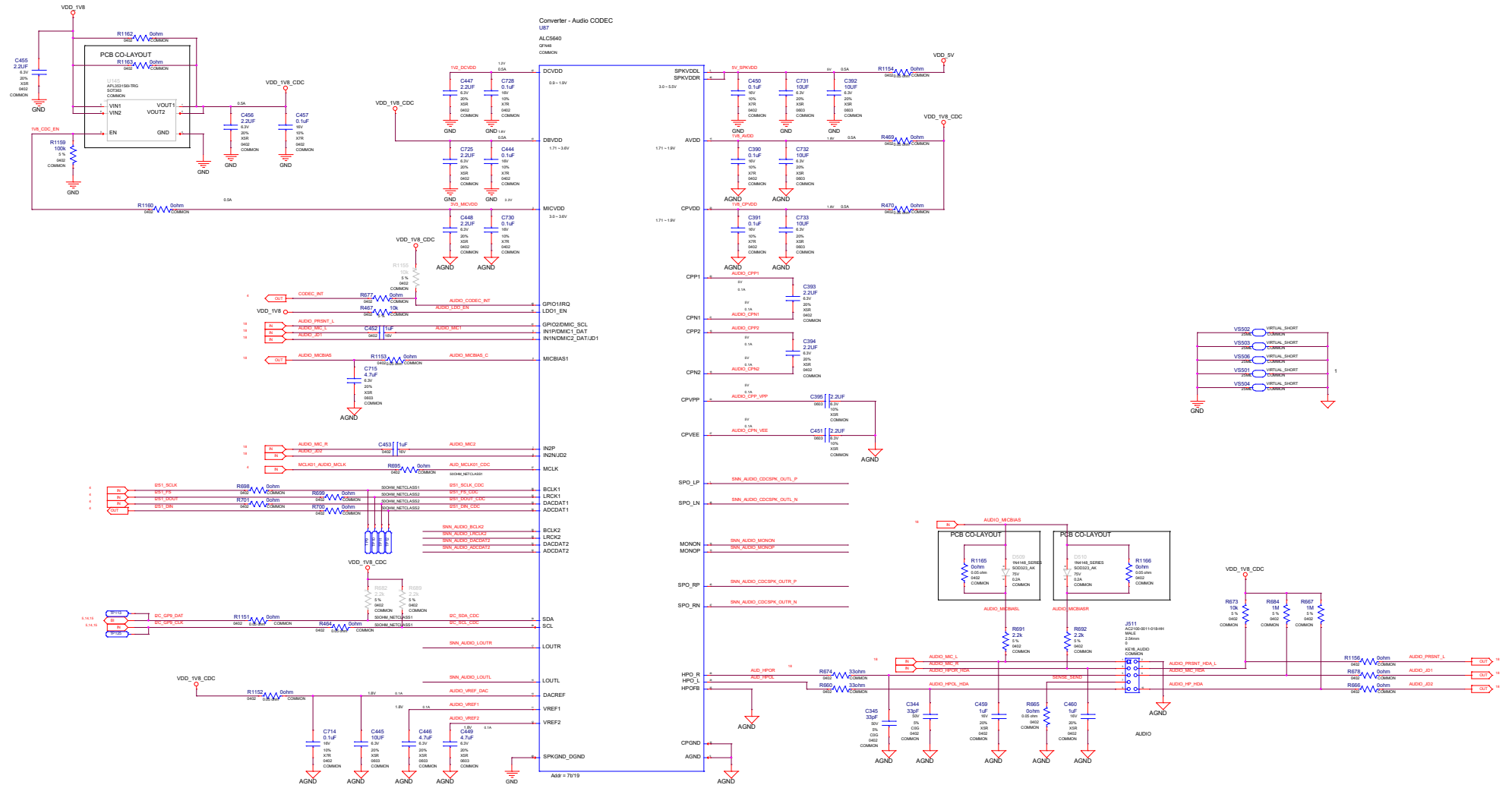




0 | B → A
1 | A → B

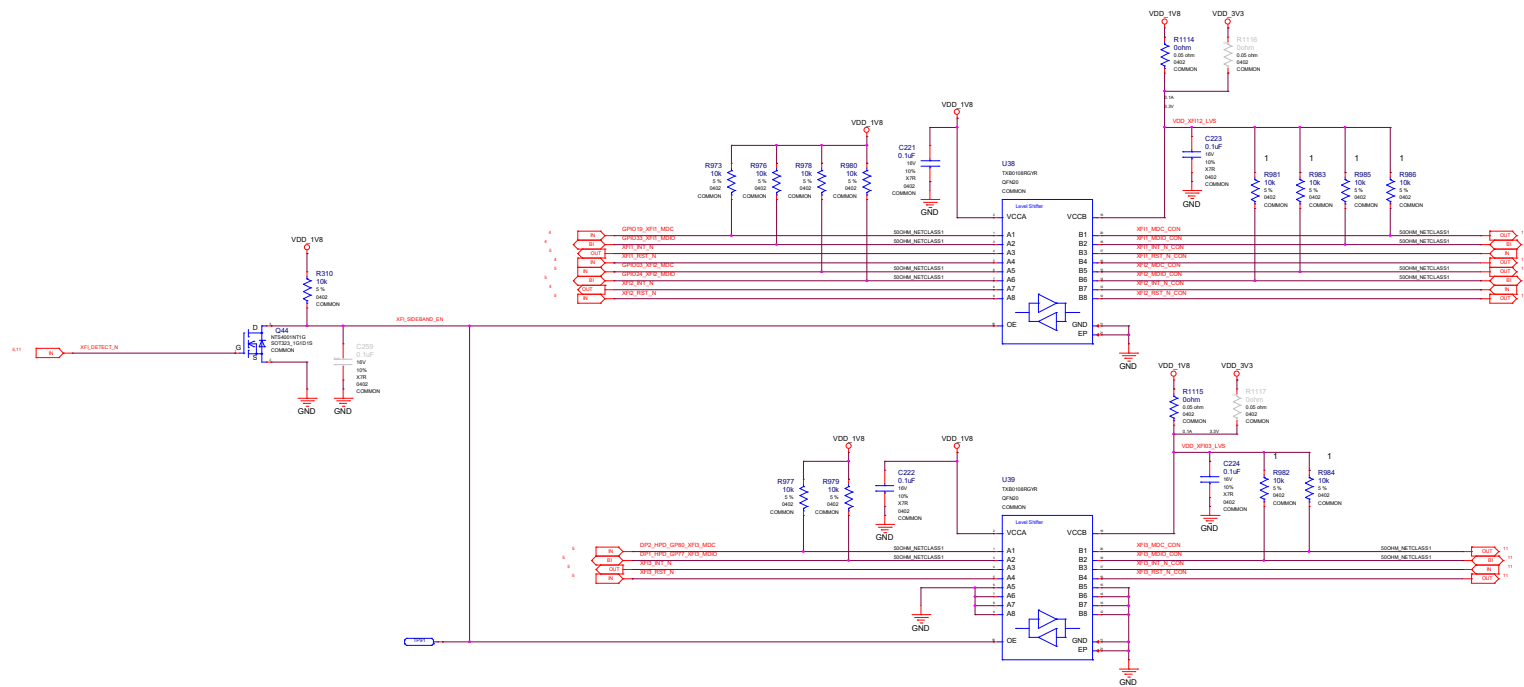
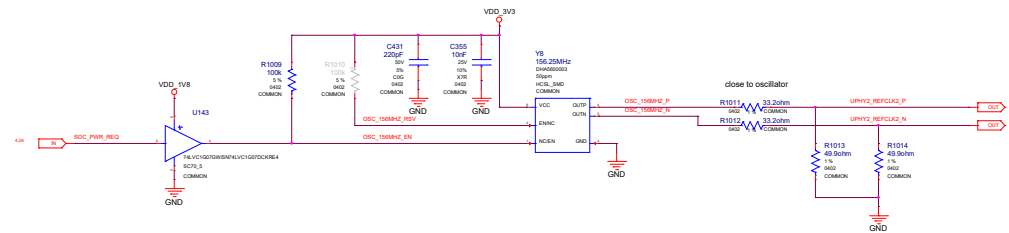


CODEC (I2S1)

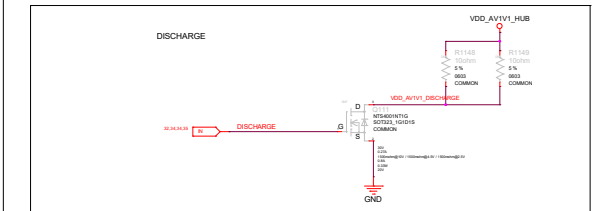
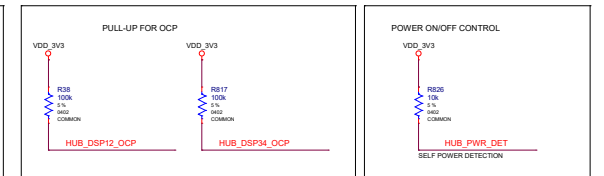
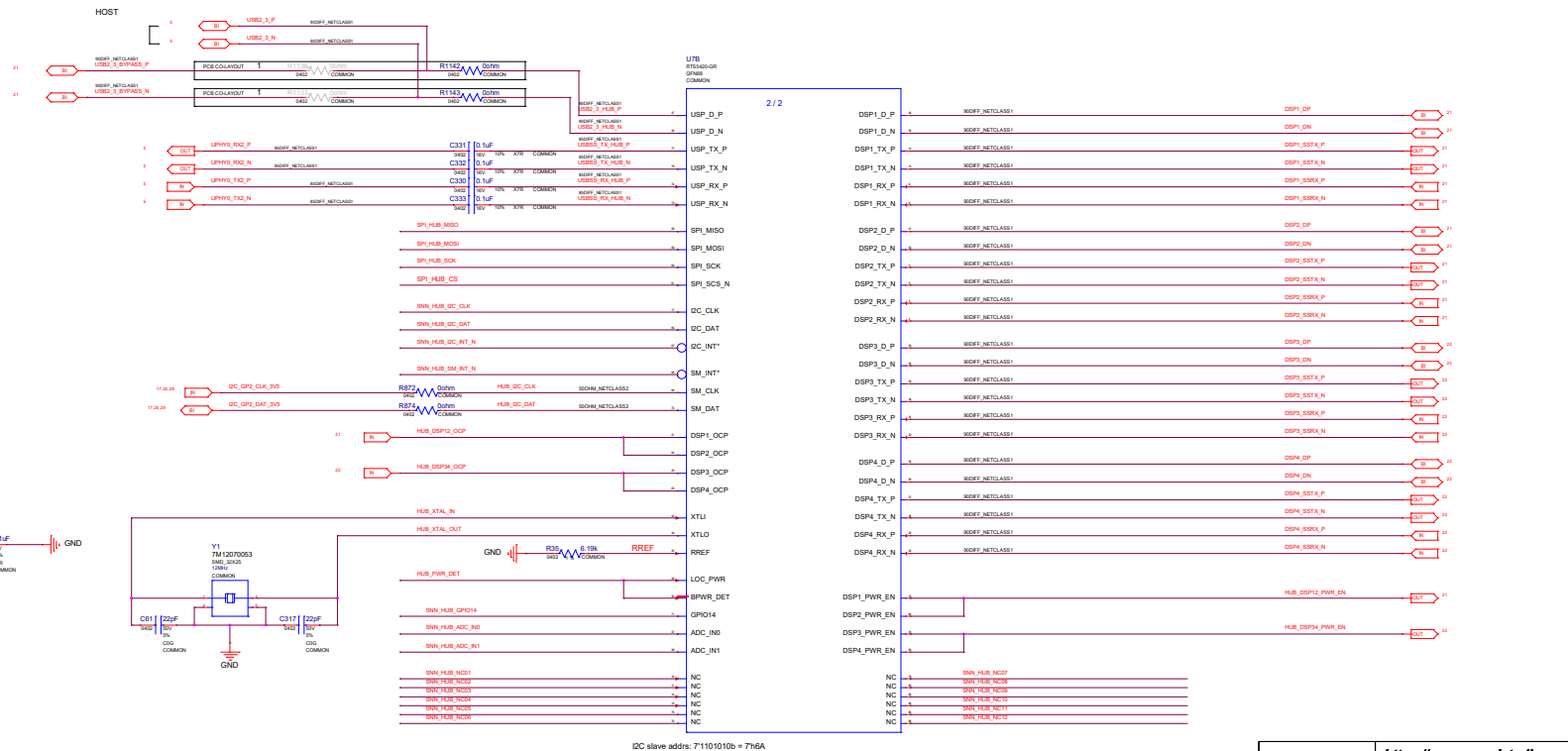
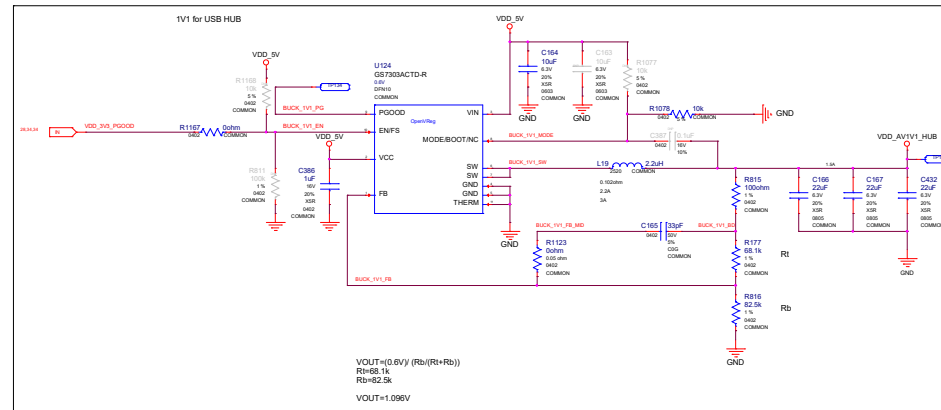
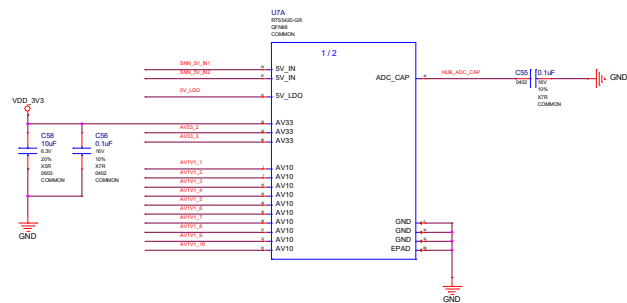
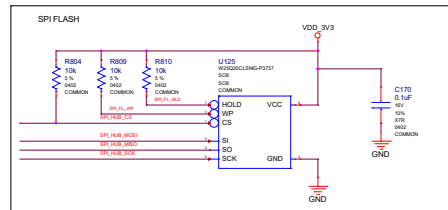
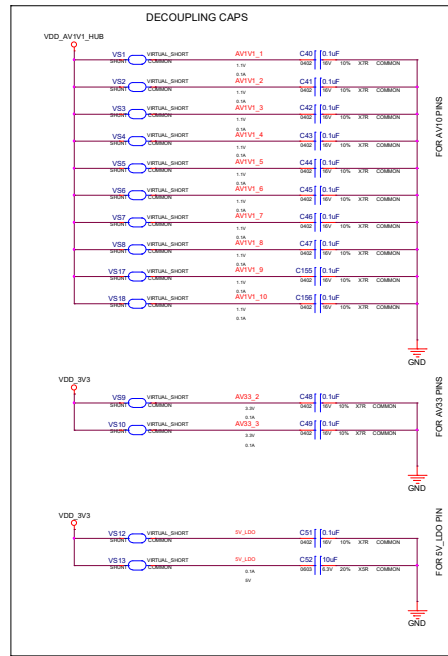


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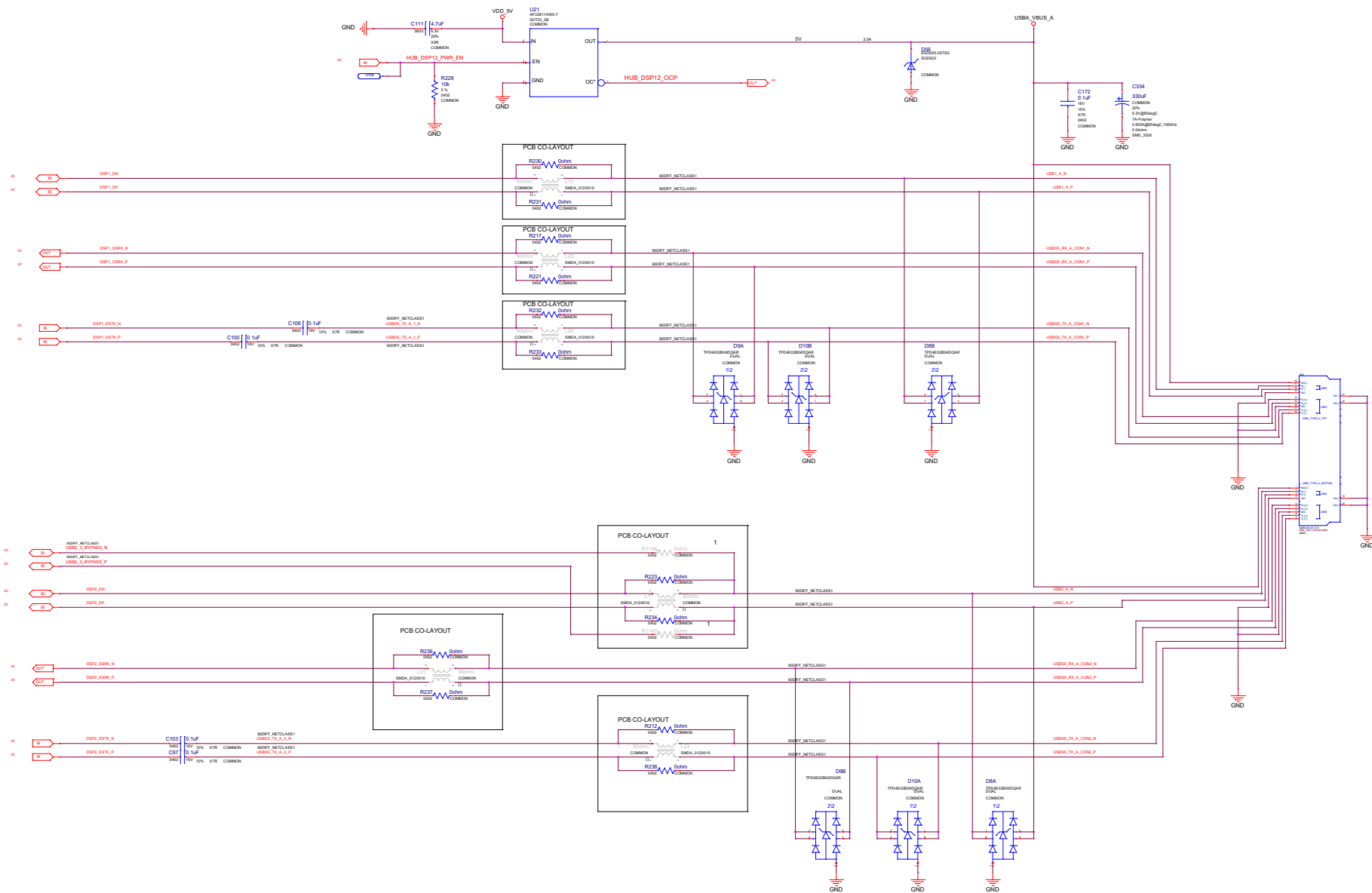
MGBE reference clock



USB3.1 GEN2 HUB x4 PORTS

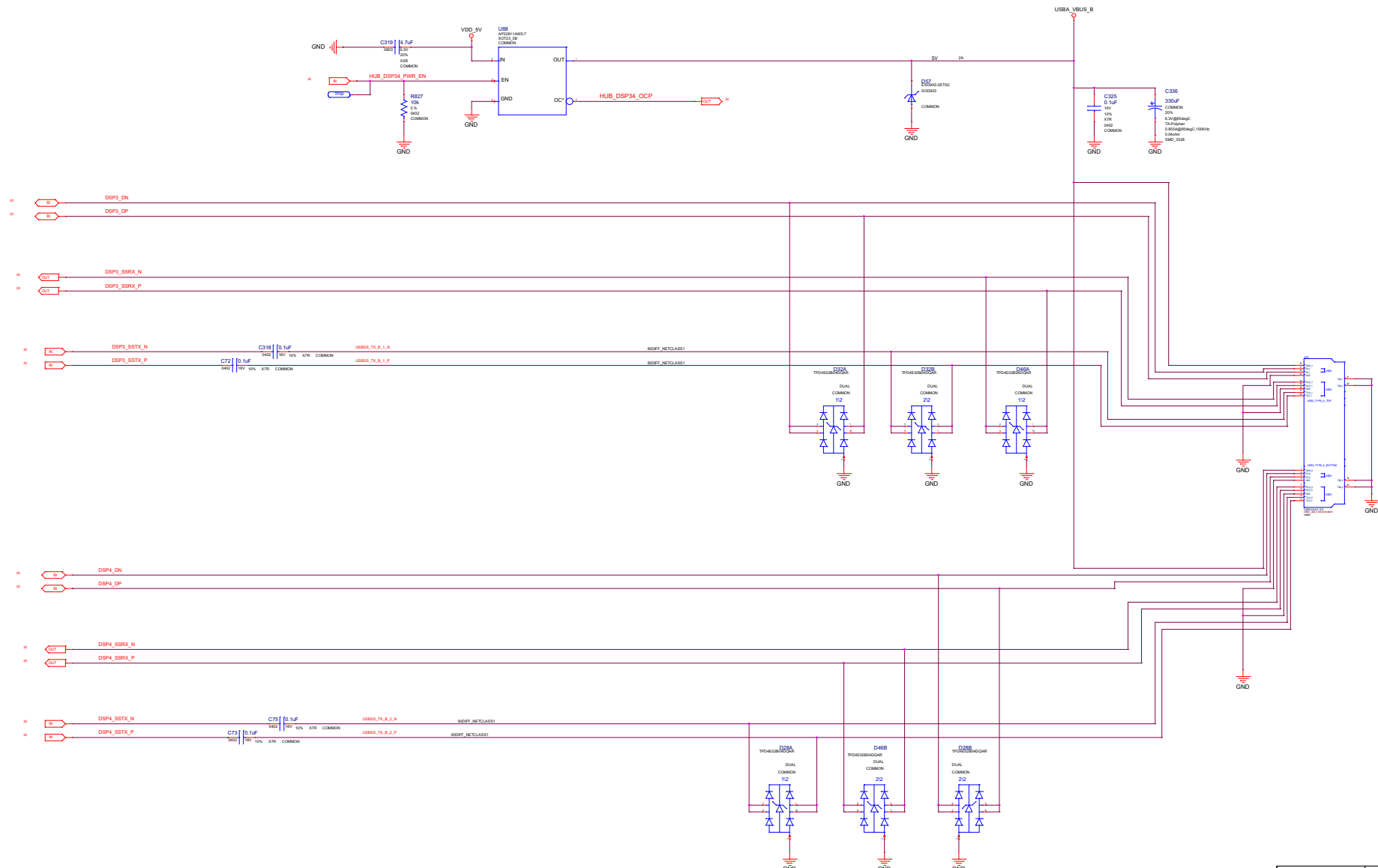


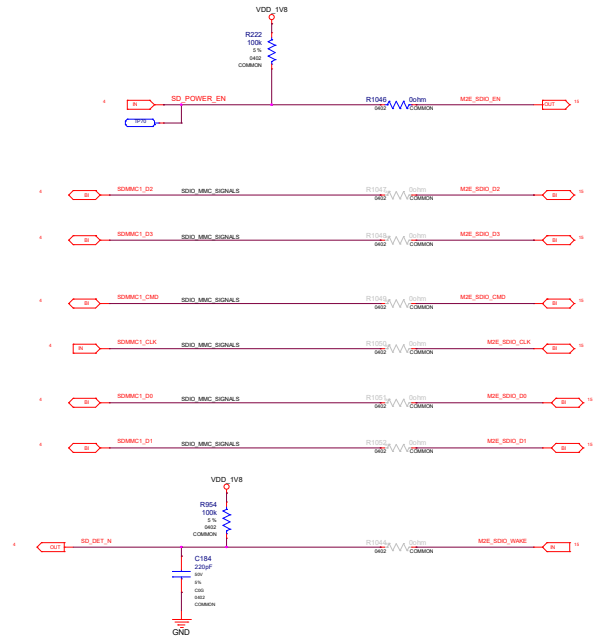
USB3.1 TYPE-A x2 (A)



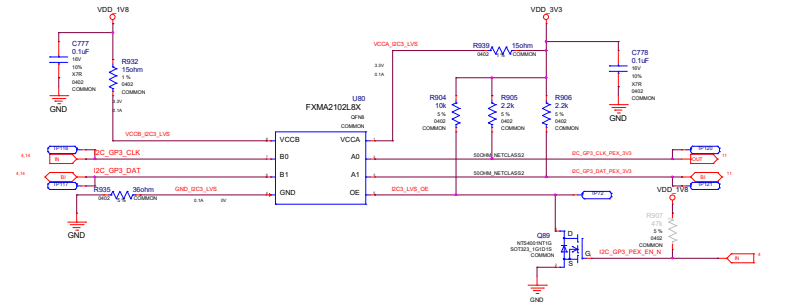
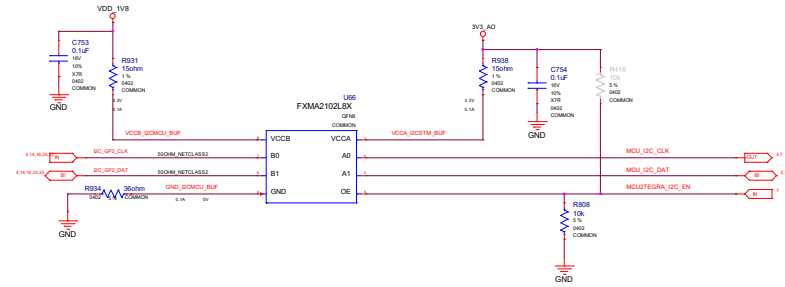
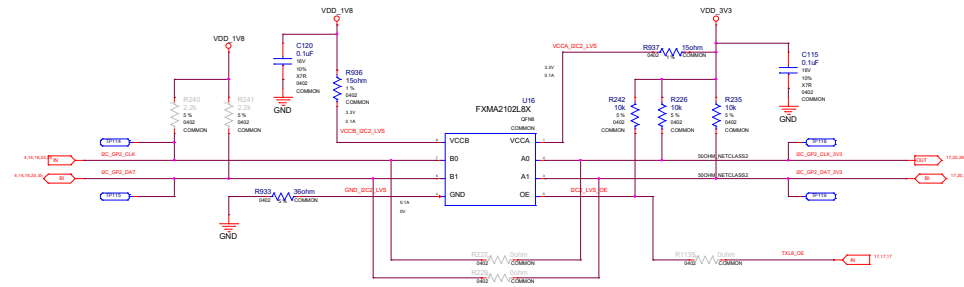
From HUB

USB3.1 TYPE-A x2 (B)



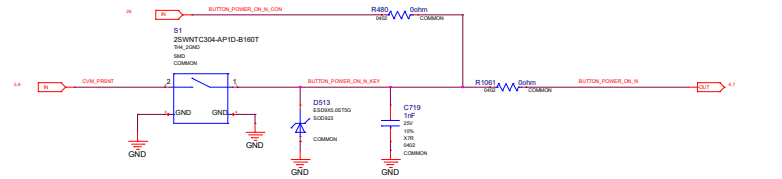


I2C GP2 LEVEL SHIFTER/REPEATER

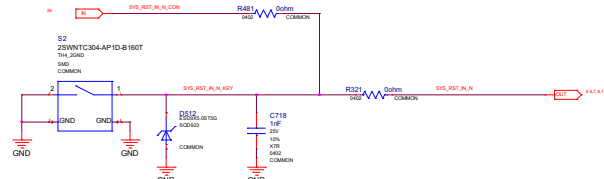


Buttons & Fan

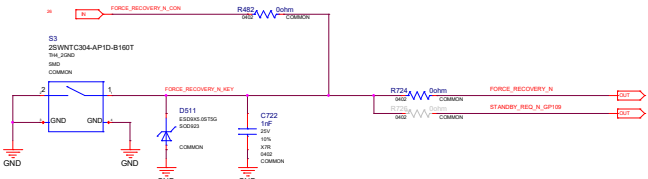
POWER



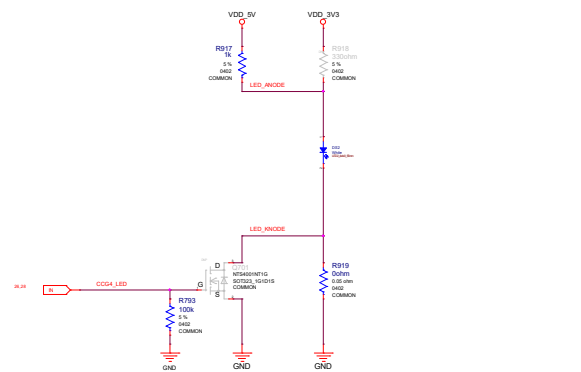
RESET



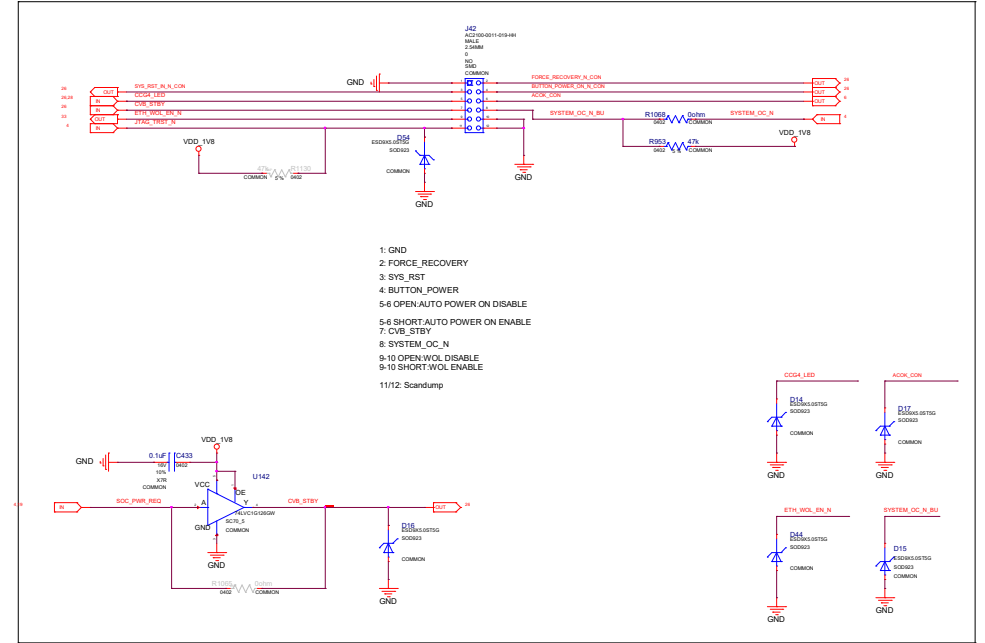
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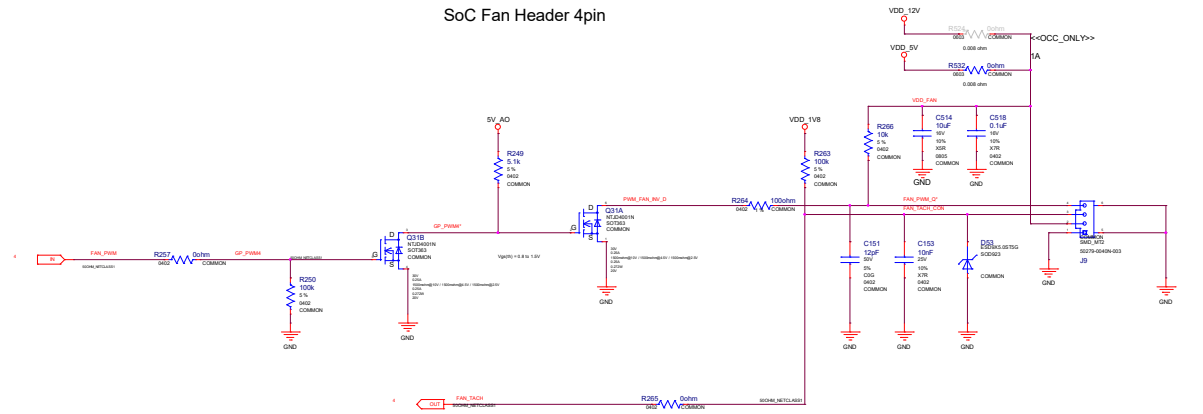
LED



Automation Header

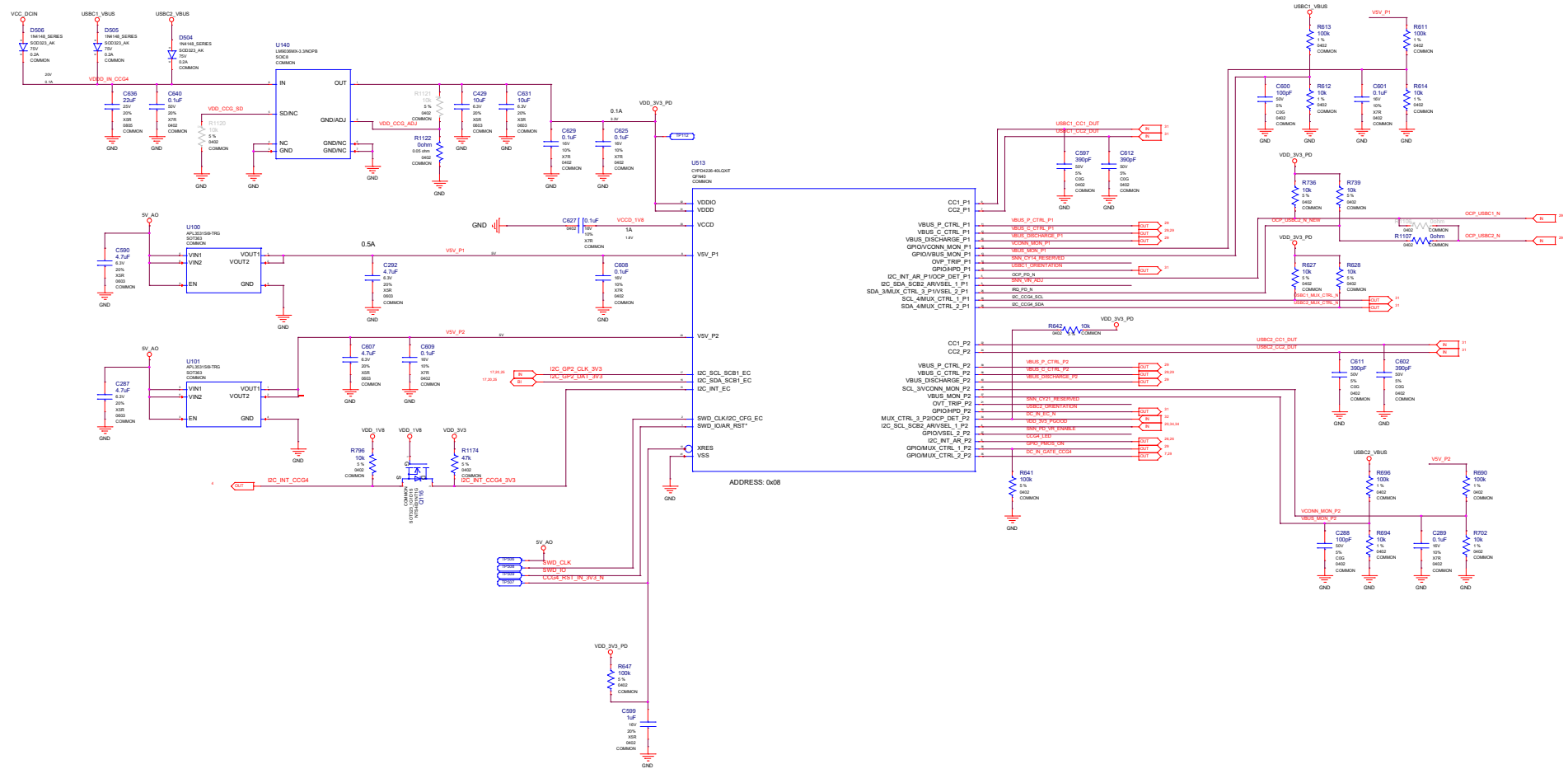


SoC Fan Header 4pin



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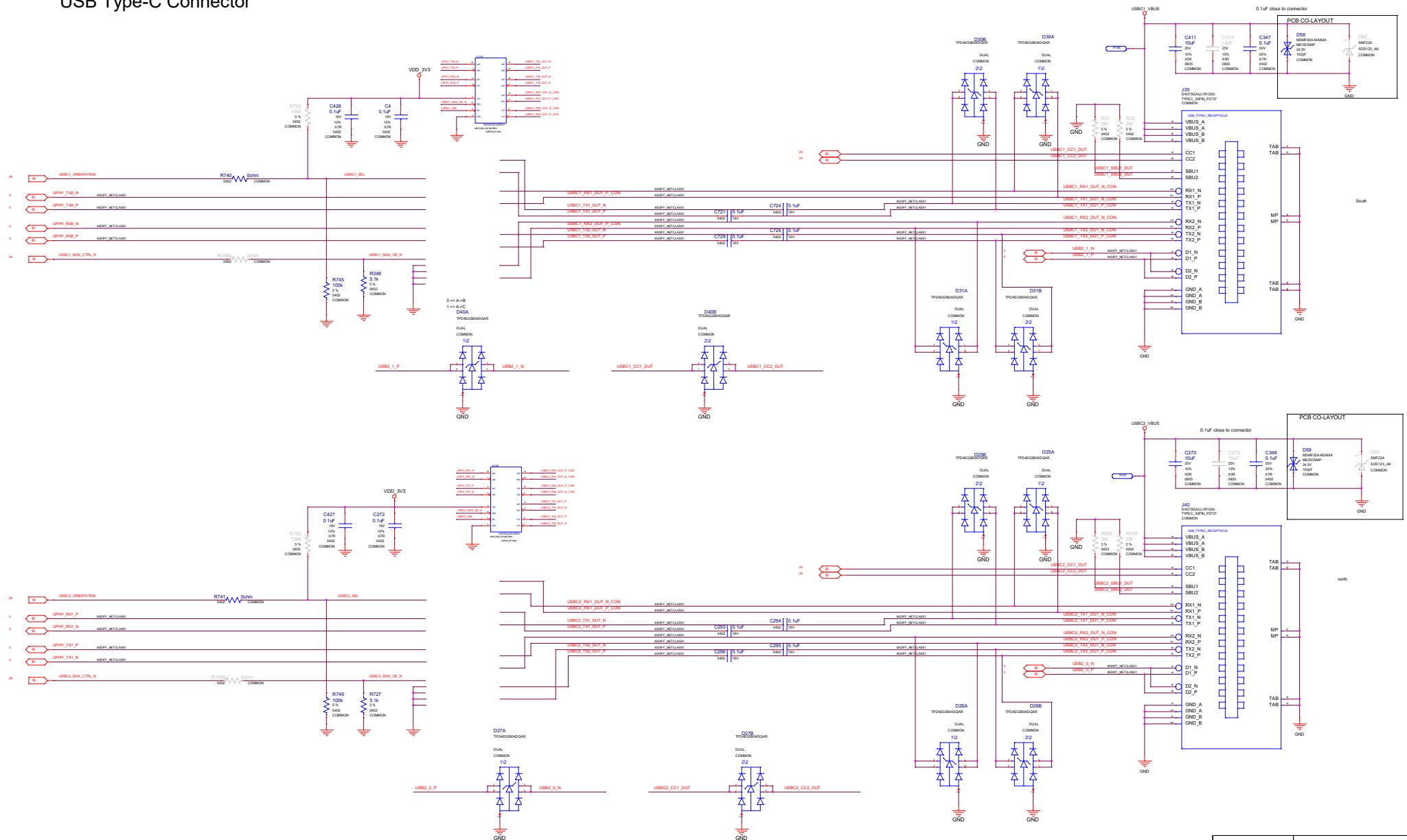
USB PD Controller



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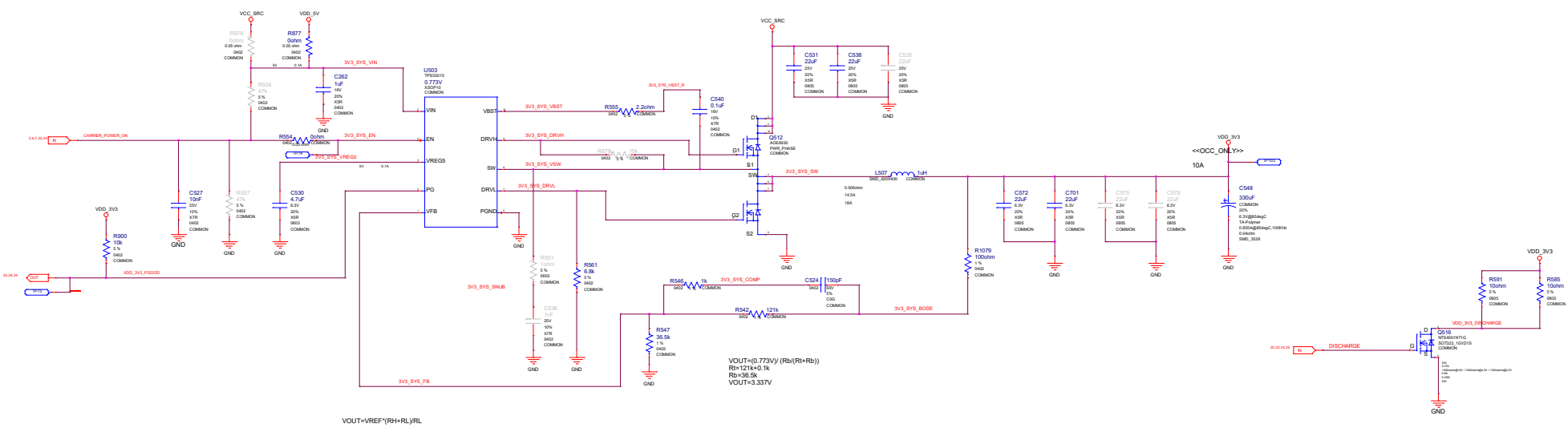
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USB Type-C Connector

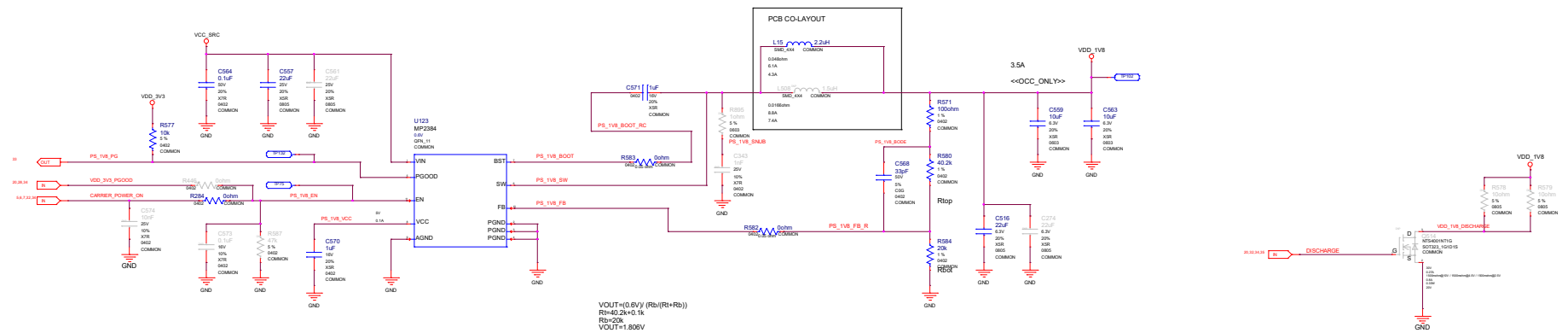


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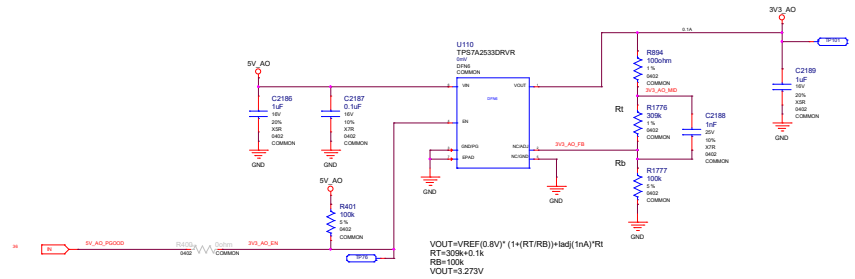
3V3



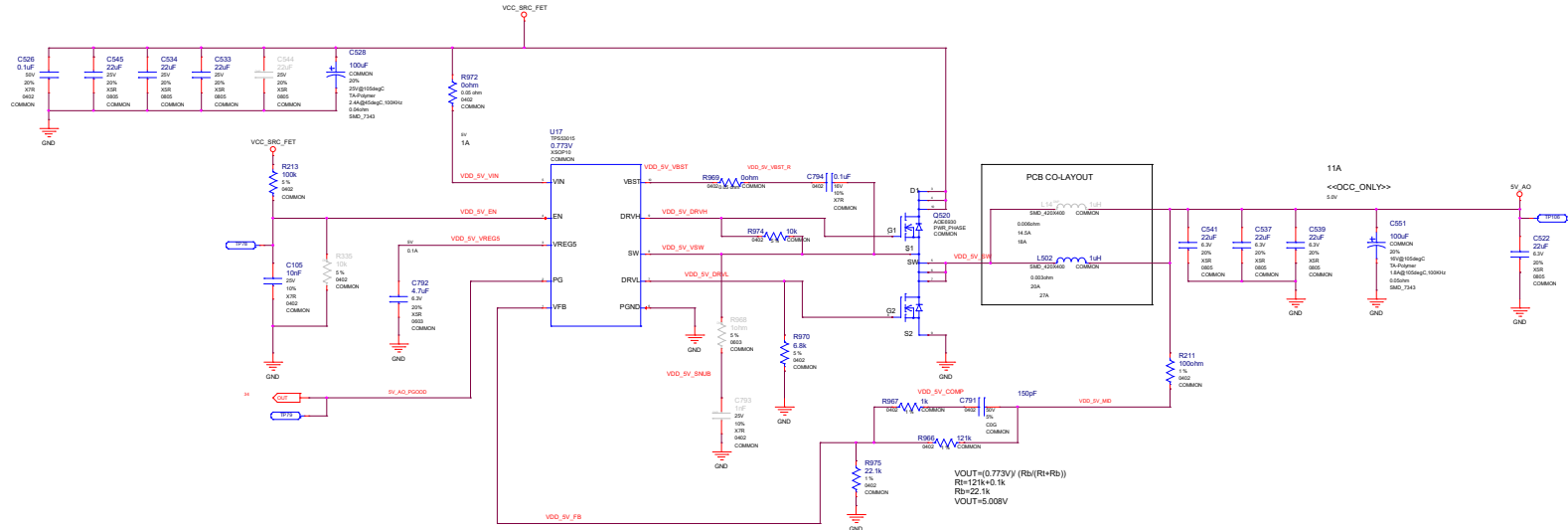
1V8



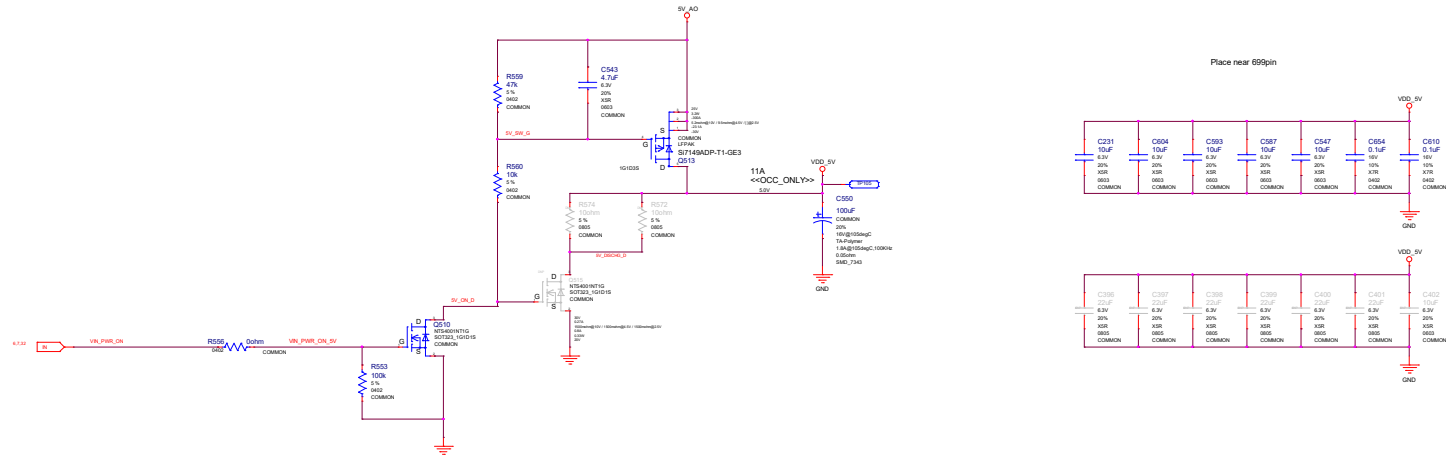
3V3_AO

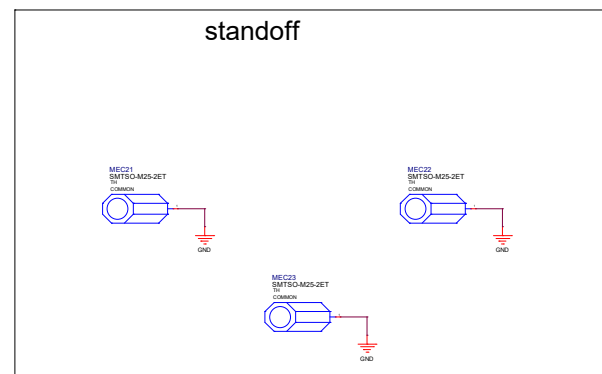
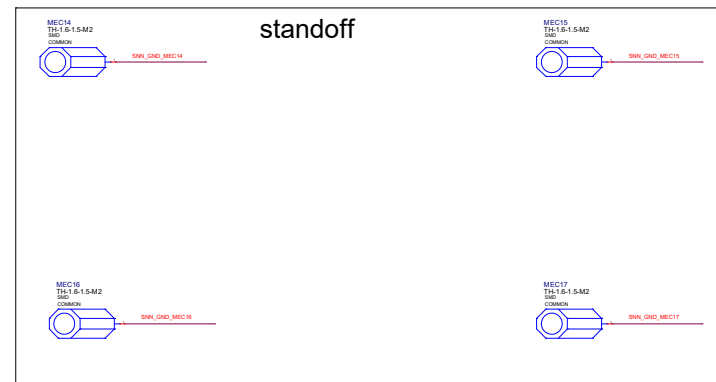
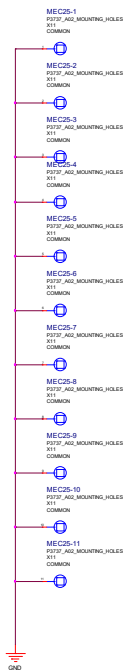


5V_AO



VDD_5V





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