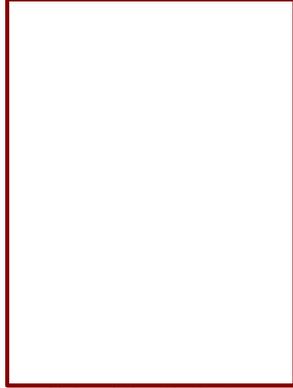
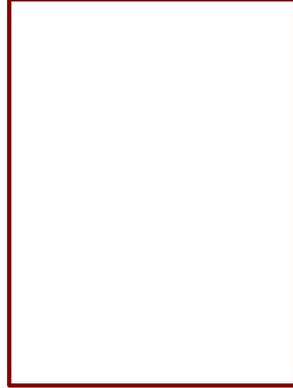


01 Descriptions



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02 Block Diagram



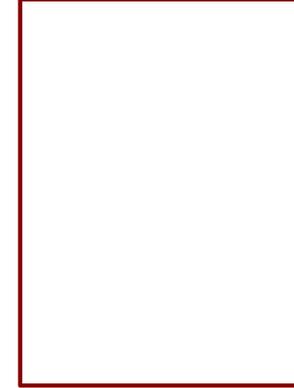
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03 Power



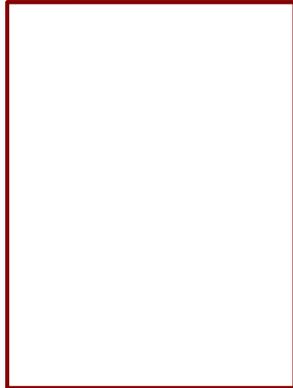
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04 ESP32-S3



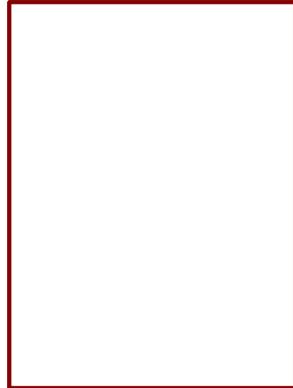
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05 Debug&Interface



File: 05 Debug&Interface.kicad_sch

06 Peripherals



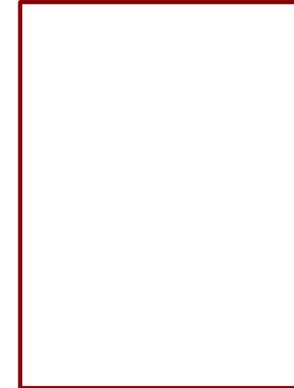
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07 50P FPC Connector



File: 07 50P FPC Connector.kicad_sch

08 24P FPC Connector



File: 08 24P FPC Connector.kicad_sch

seed studio

Sheet: /
File: reTerminal E1001.kicad_sch

Title: reTerminal E1001

CC BY-SA 4.0

Size: A4 Date: 2025-11-14

Rev: v1.2

KiCad E.D.A. 9.0.0

Id: 1/9

Version	Descriptions	Data	Name
v1.0	First Release	4/30/2025	WK
v1.2	Page4: Change the U2 ETA6003 to SY6974B Page8: Change Q9 to DMN3404L-7	11/20/2025	Harry

seed studio

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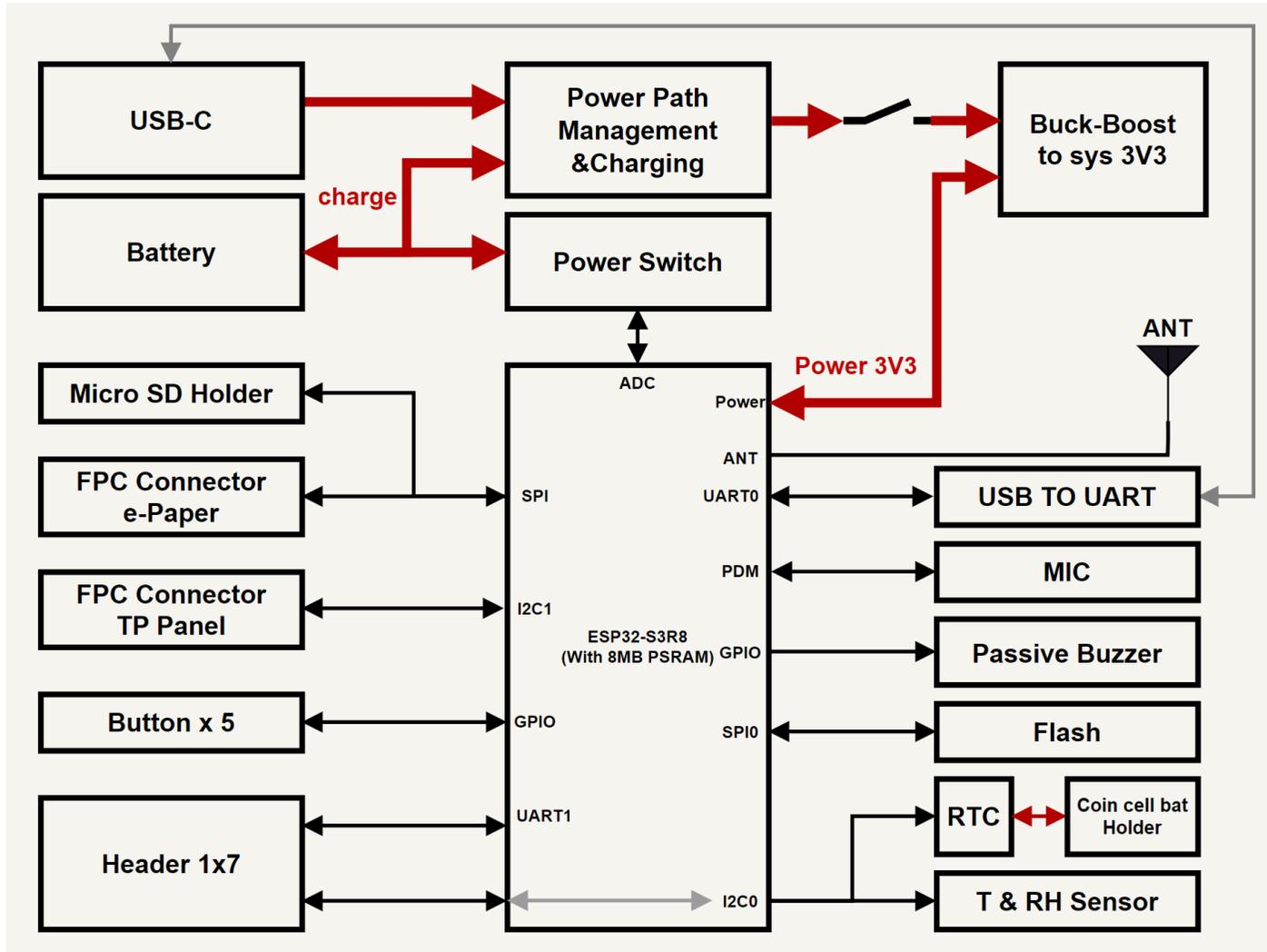
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Size: A4 Date: 2025-11-14

Rev: v1.2

KiCad E.D.A. 9.0.0

Id: 2/9



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Title: reTerminal E1001

CC BY-SA 4.0

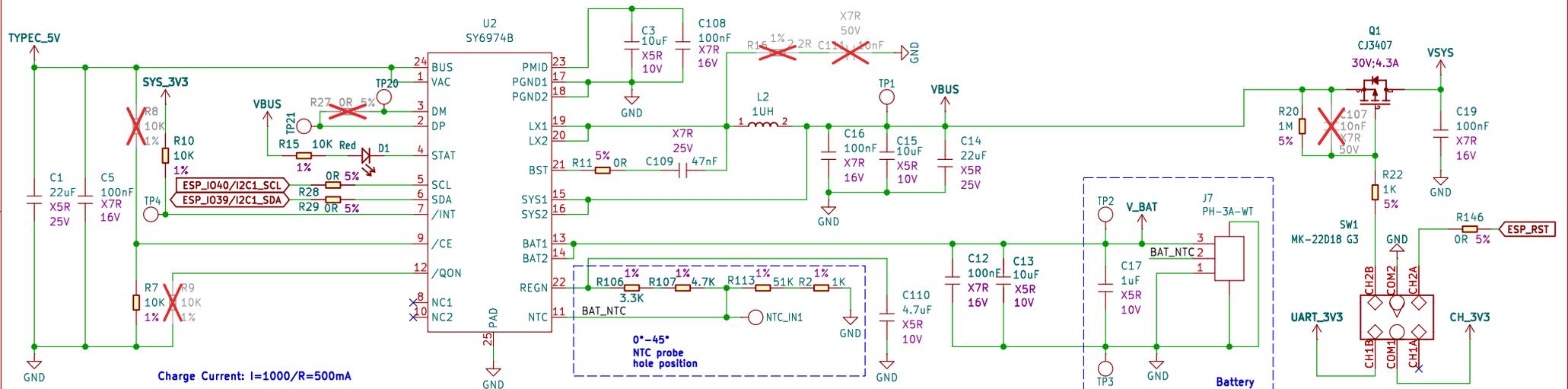
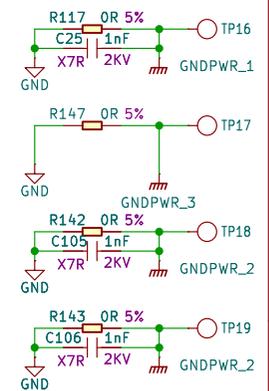
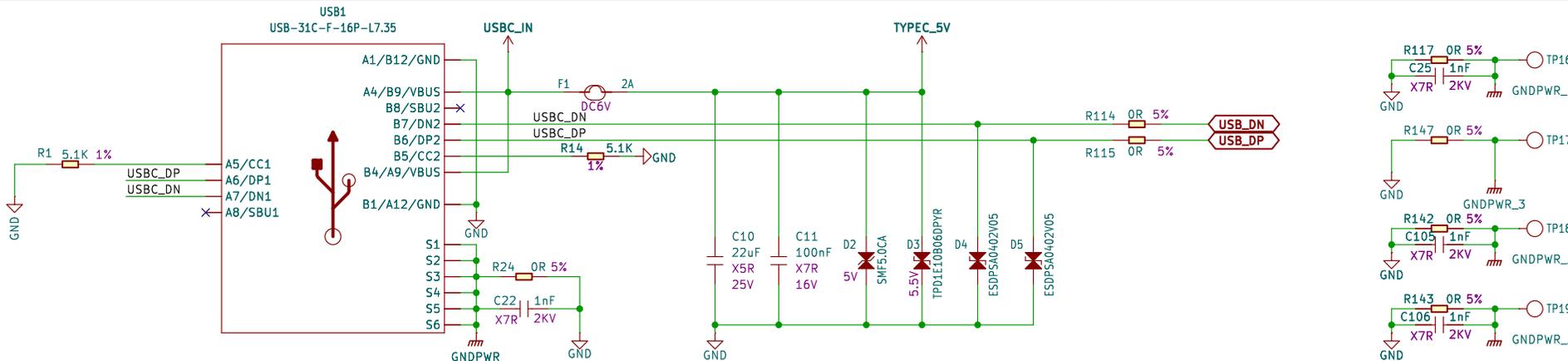
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Rev: v1.2

KiCad E.D.A. 9.0.0

Id: 3/9

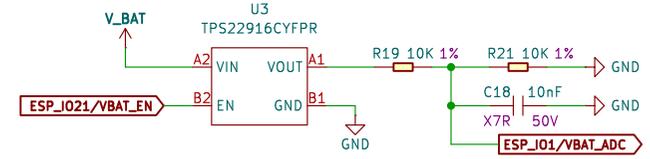
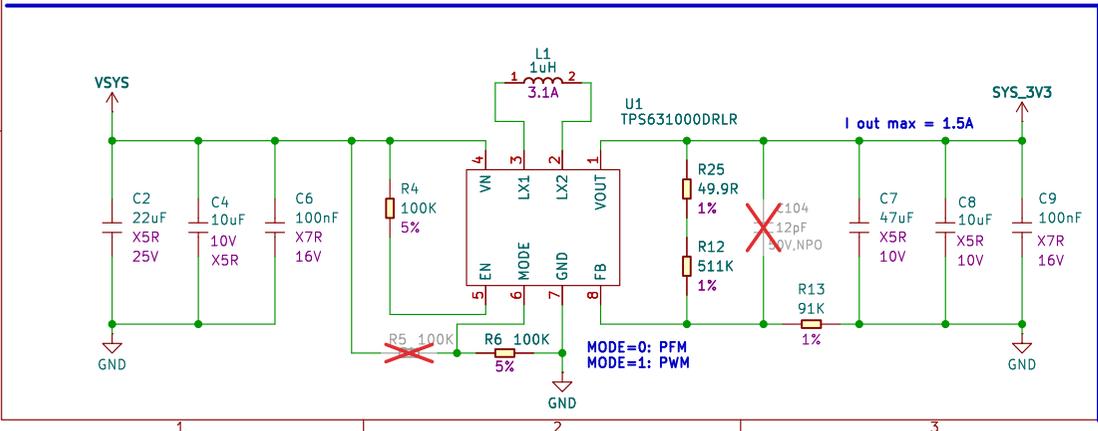
POWER



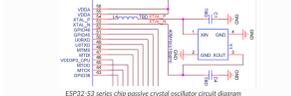
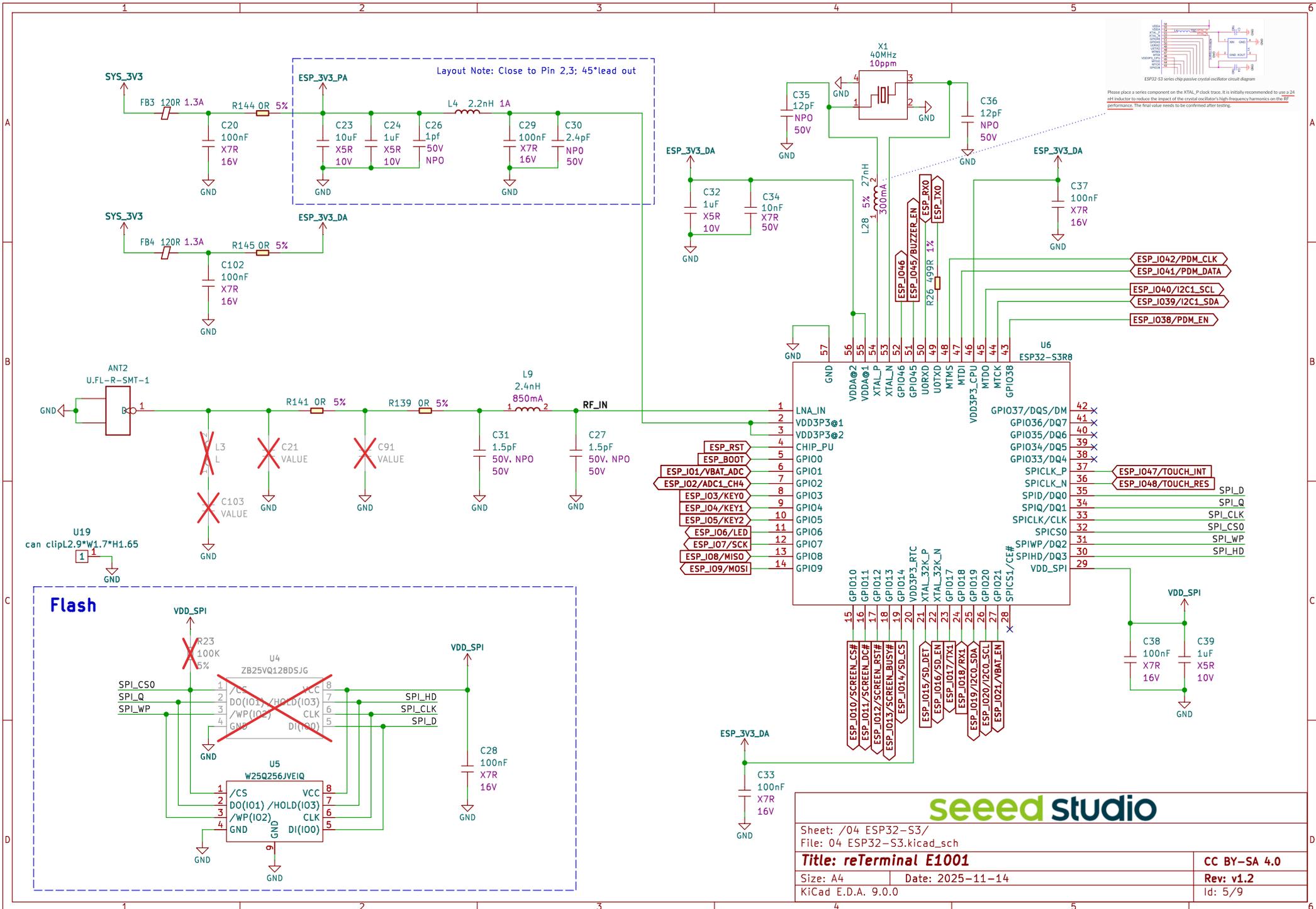
Charge Current: $I=1000/R=500\text{mA}$

0°-45° NTC probe hole position

Battery



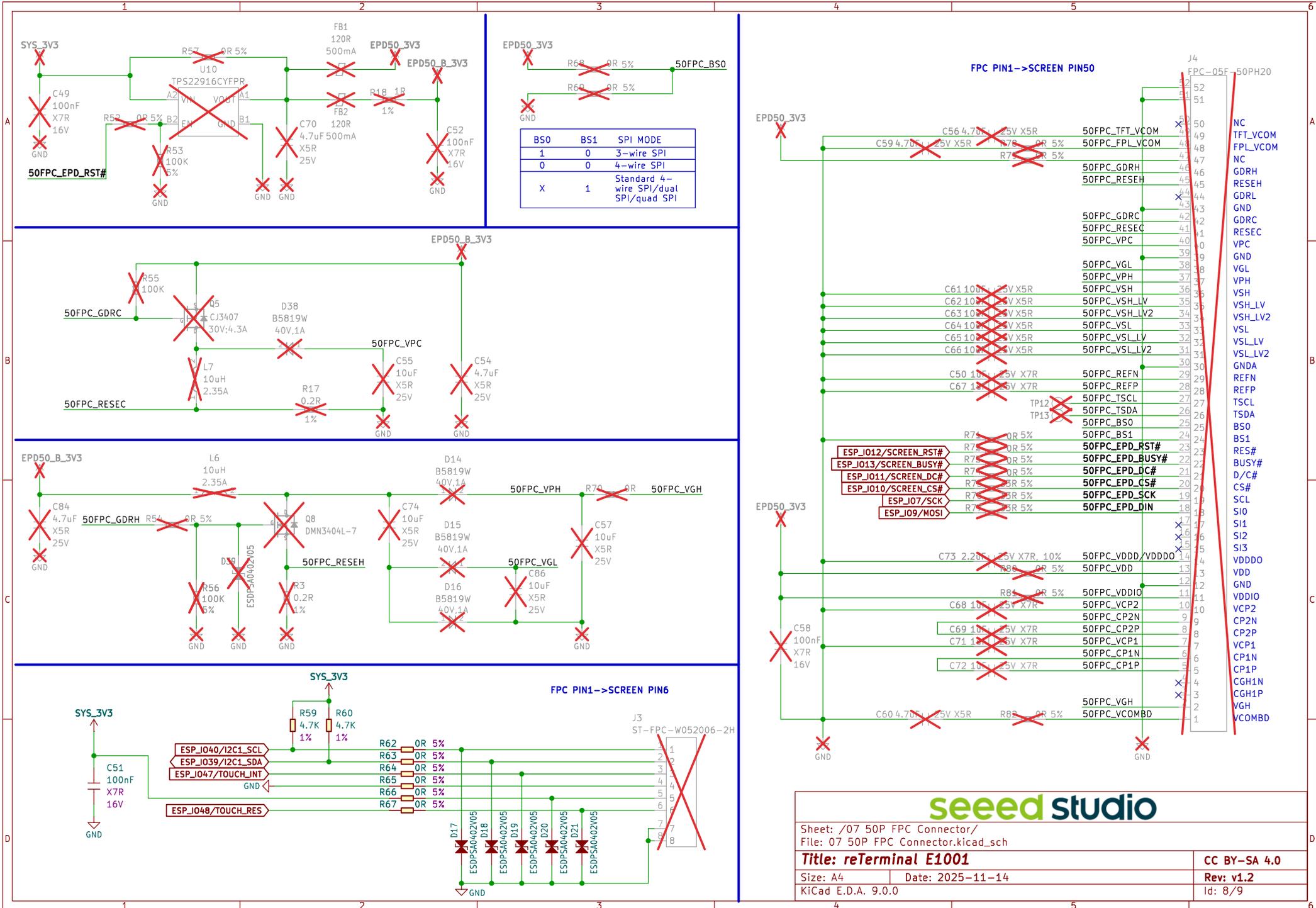
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Title: reTerminal E1001		Rev: v1.2	
Size: A4	Date: 2025-11-14	KiCad E.D.A. 9.0.0	
Id: 4/9			



Please place a series component on the XTAL_P clock trace. It is initially recommended to use a 24 nH inductor to reduce the impact of the crystal oscillator's high-frequency harmonics on the RF performance. The final value needs to be confirmed after testing.

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Sheet: /04 ESP32-S3/		CC BY-SA 4.0	
File: 04 ESP32-S3.kicad_sch		Rev: v1.2	
Size: A4	Date: 2025-11-14	Id: 5/9	
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FPC PIN1-->SCREEN PIN24

